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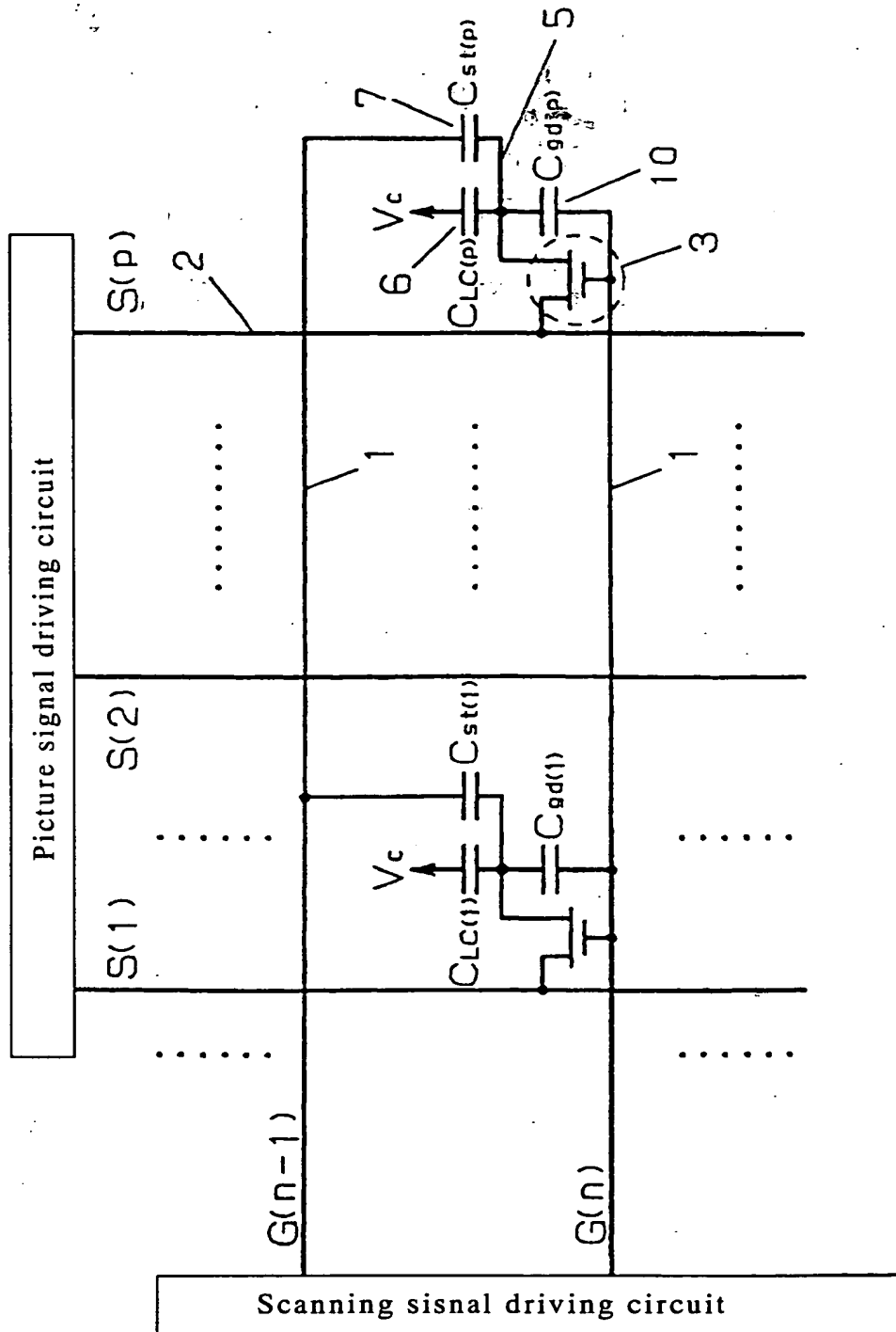


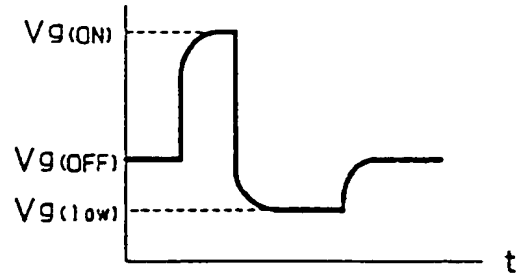
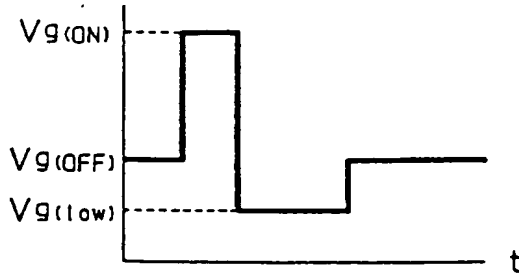
FIG. 1

(Power feeding edge)

(Terminal edge)

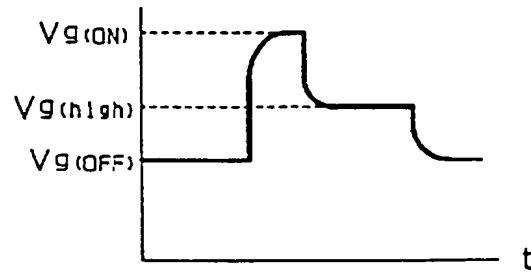
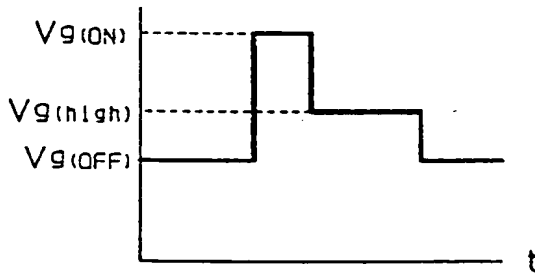
Gate voltage of
preceding line

Gate voltage of
preceding line



Gate voltage of
present line

Gate voltage of
present line



Pixel voltage

Pixel voltage

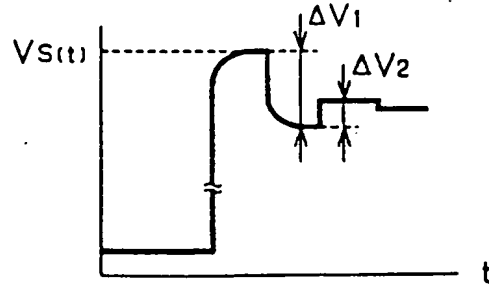
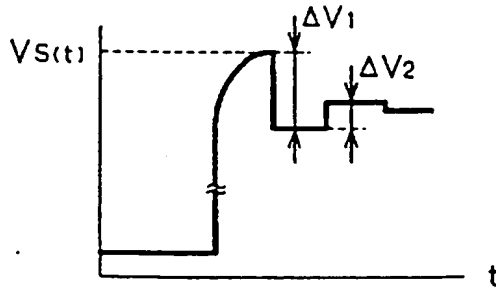


FIG. 2

(Power feeding edge)

(Terminal edge)

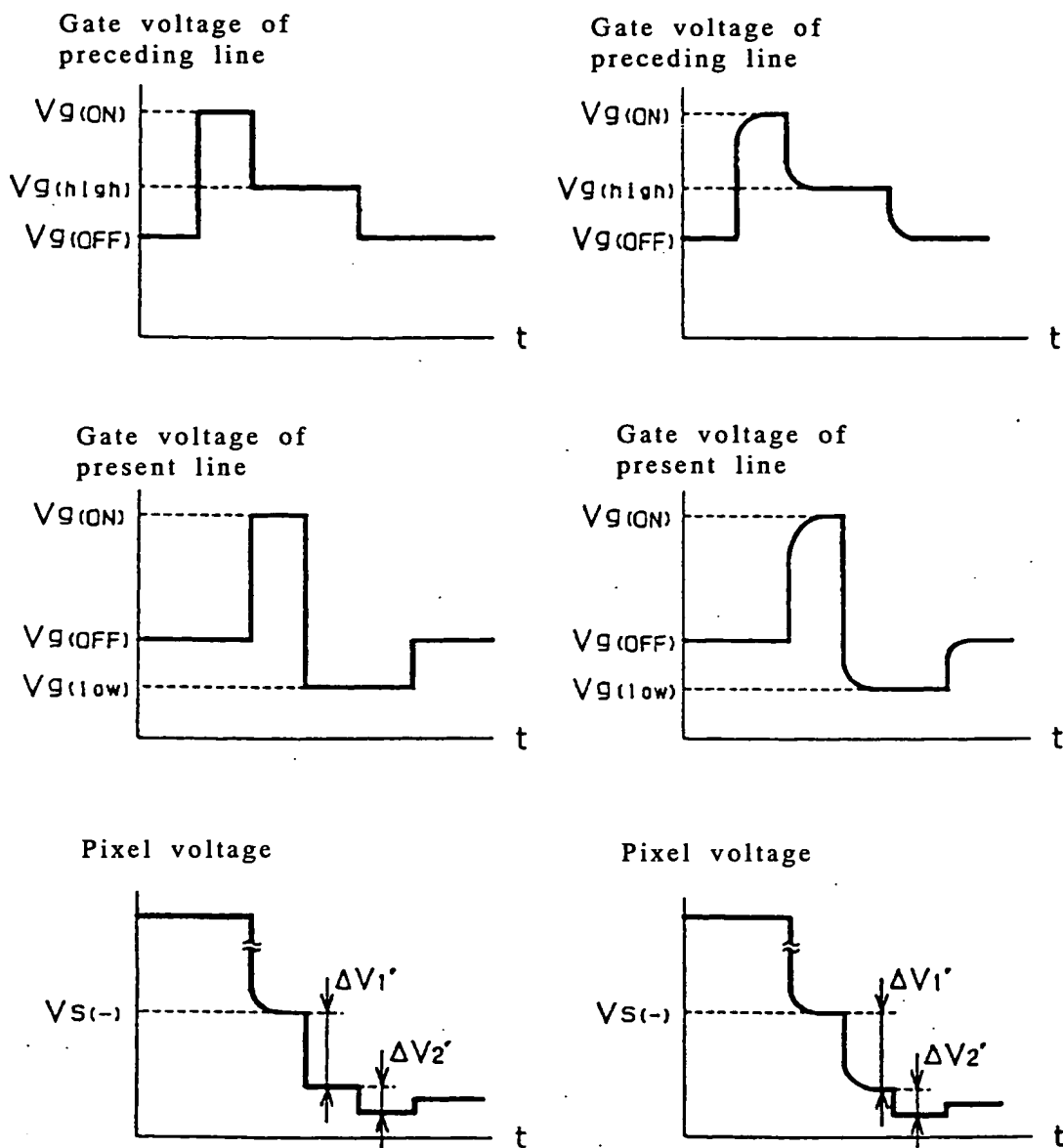
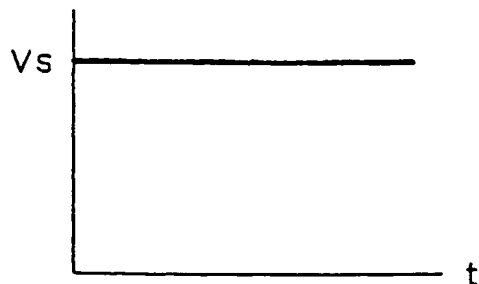


FIG. 3

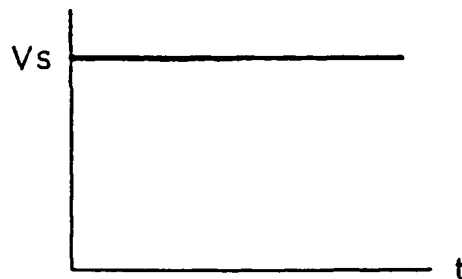
(Power feeding edge)

(Terminal edge)

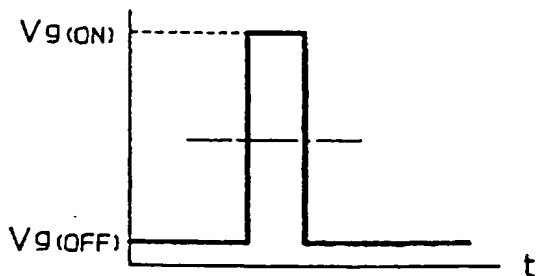
Source voltage



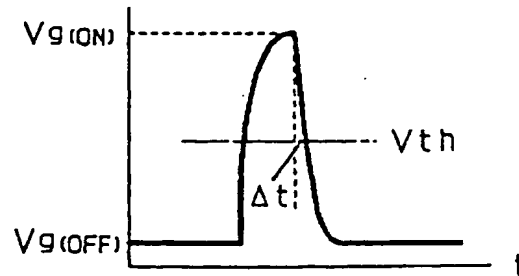
Source voltage



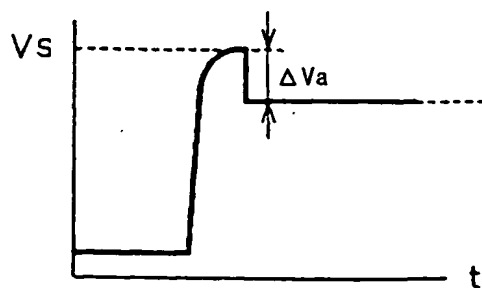
Gate voltage



Gate voltage



Pixel voltage



Pixel voltage

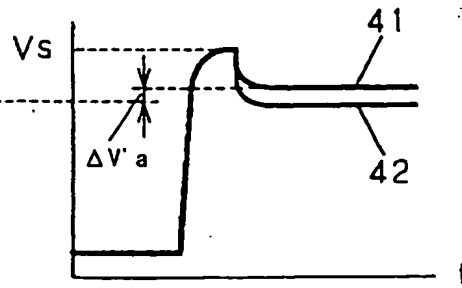
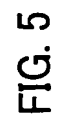


FIG. 4



(Power feeding edge)

(Terminal edge)

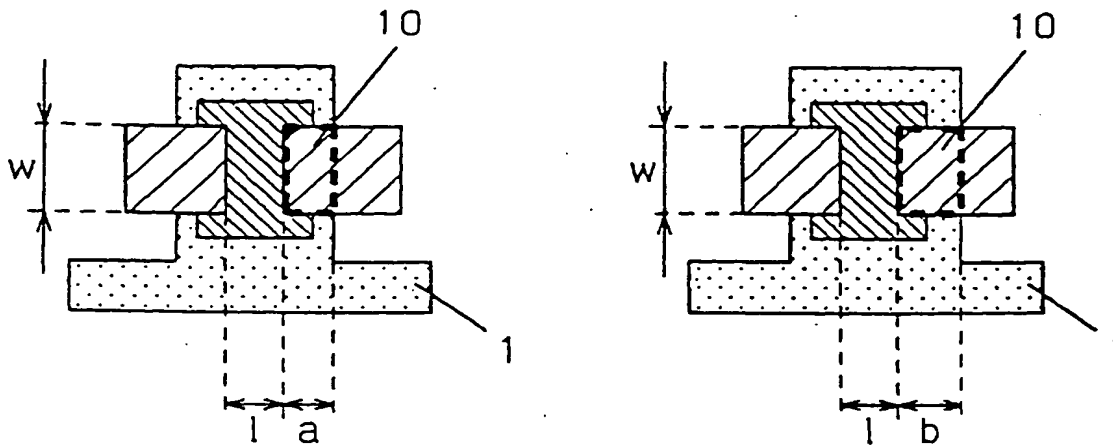


FIG. 6

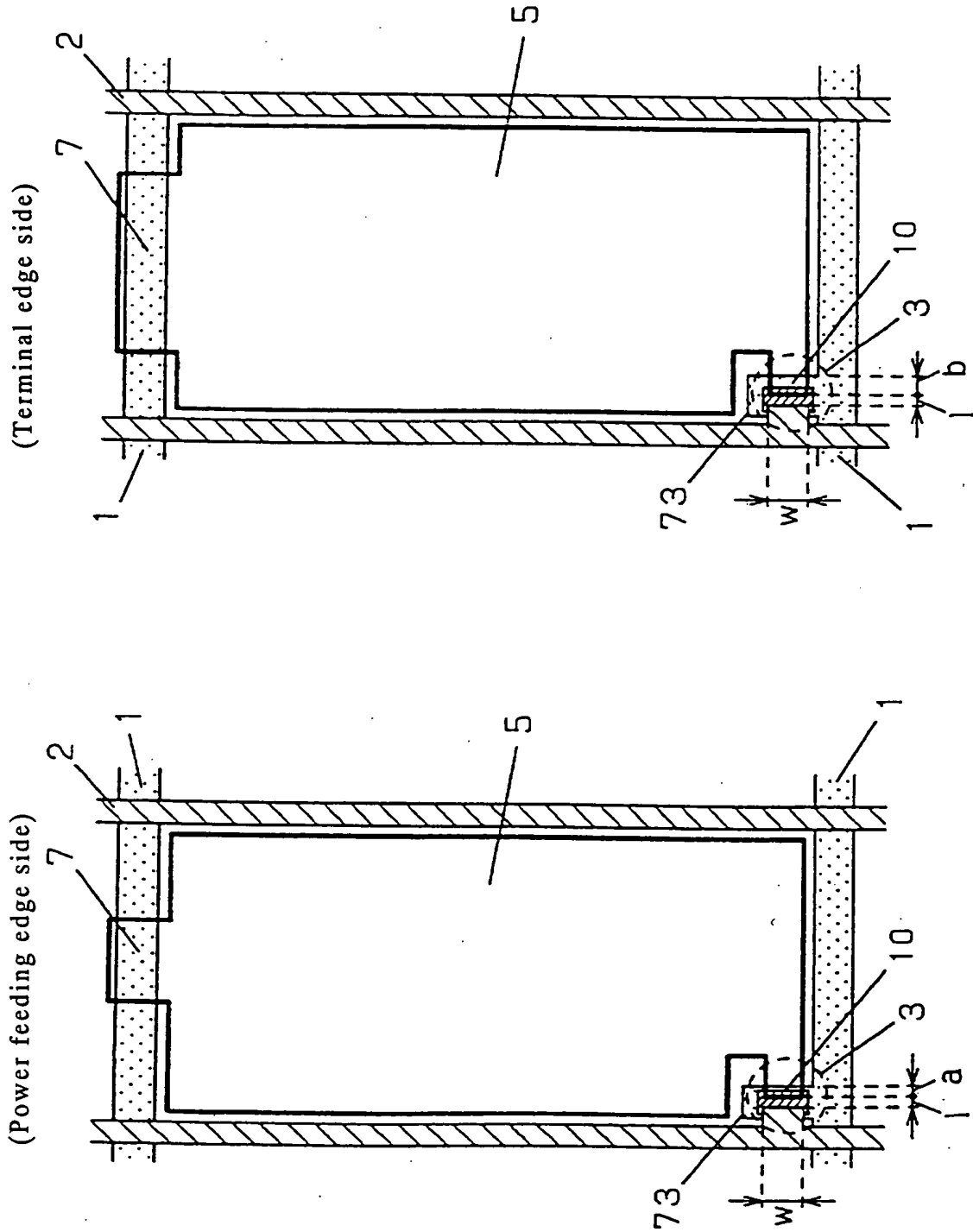


FIG. 7

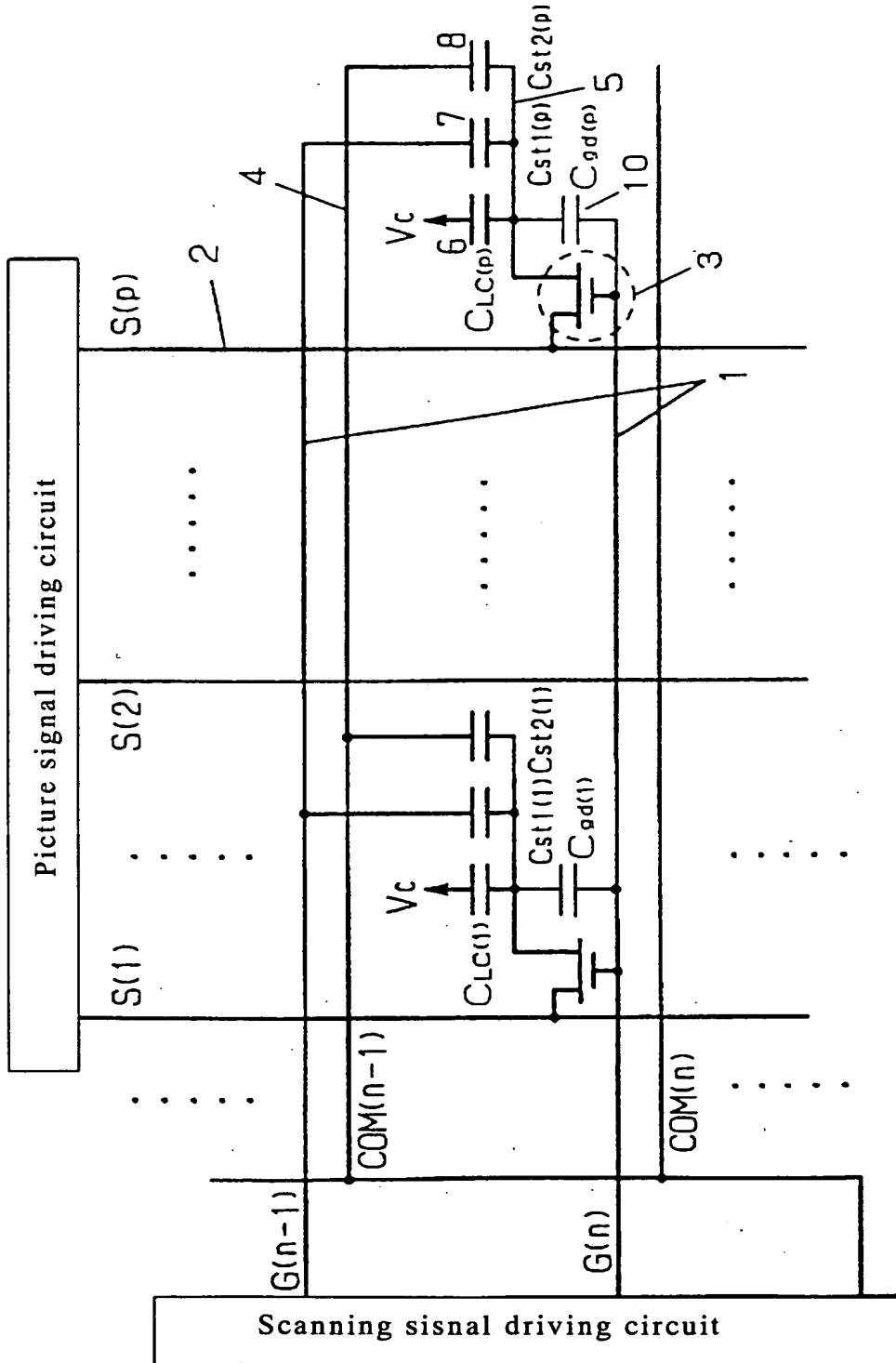


FIG. 8

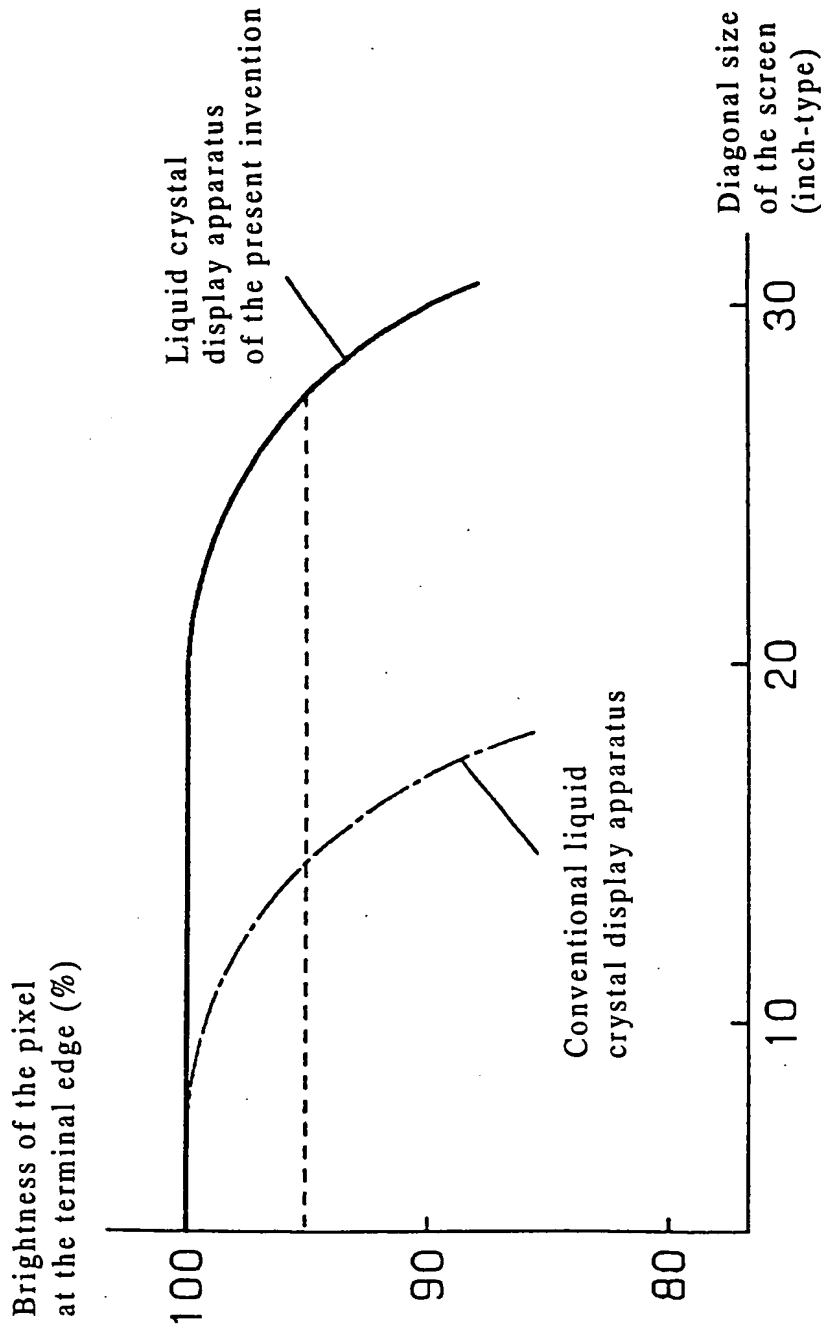
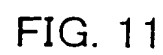


FIG. 9



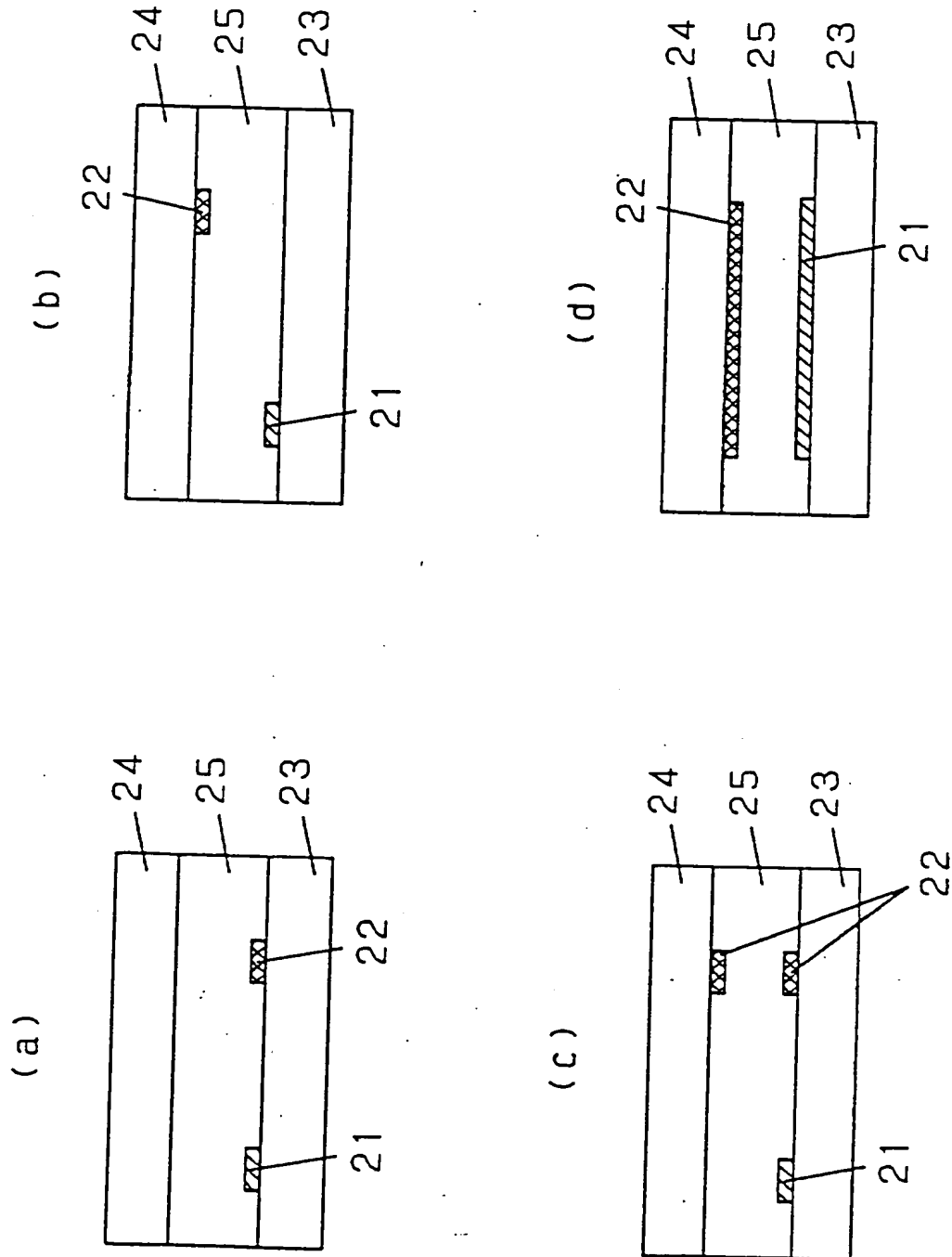


FIG. 12

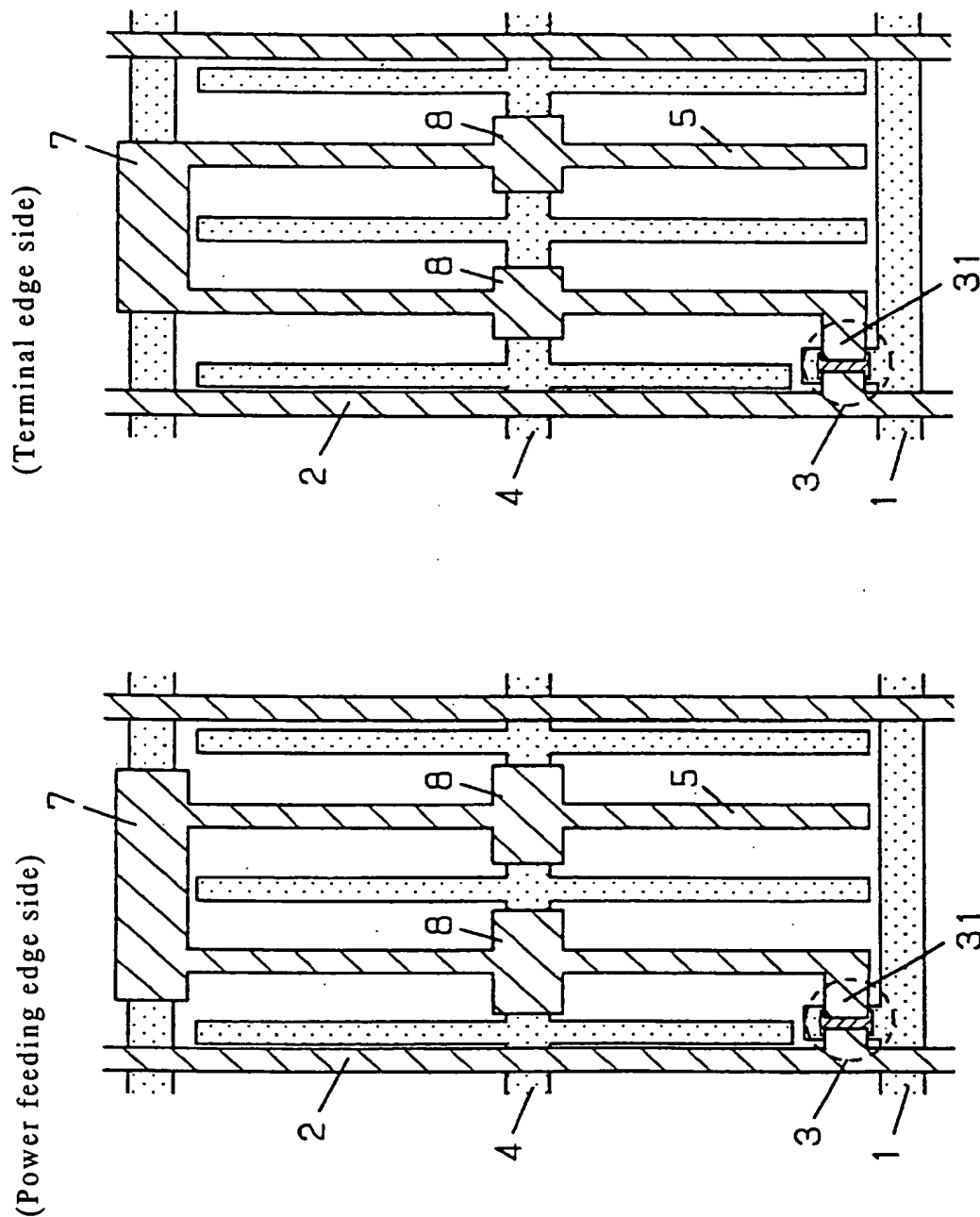


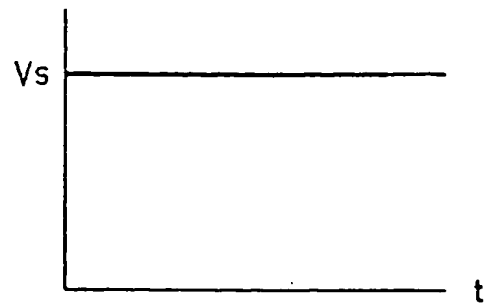
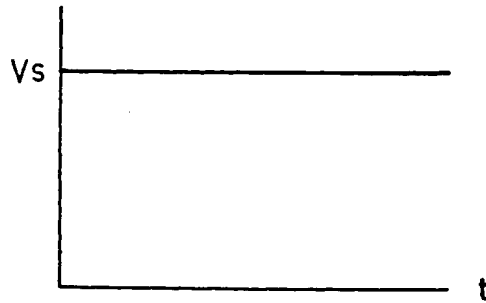
FIG. 13

(Power feeding edge)

(Terminal edge)

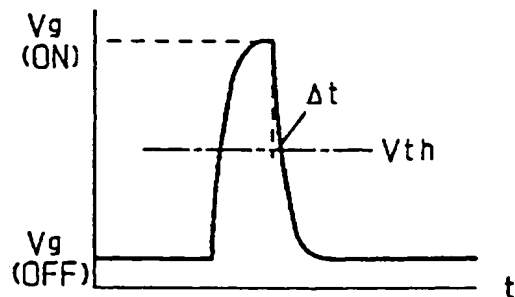
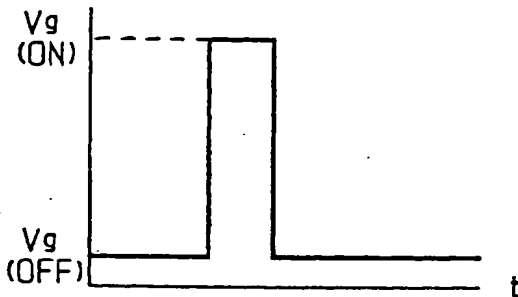
Source voltage

Source voltage



Gate voltage

Gate voltage



Pixel voltage

Pixel voltage

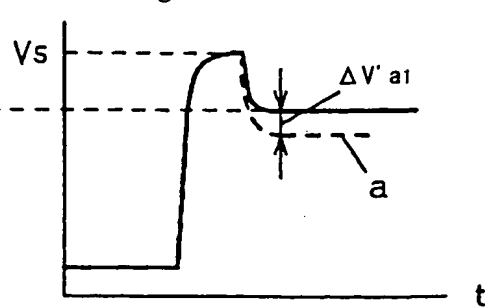
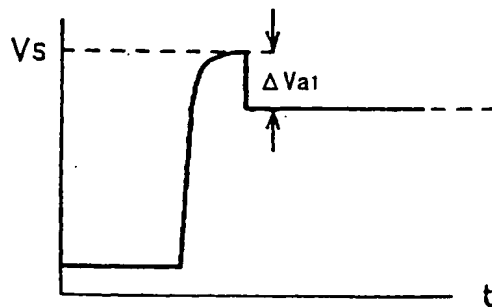


FIG. 14

(Power feeding edge)

(Terminal edge)

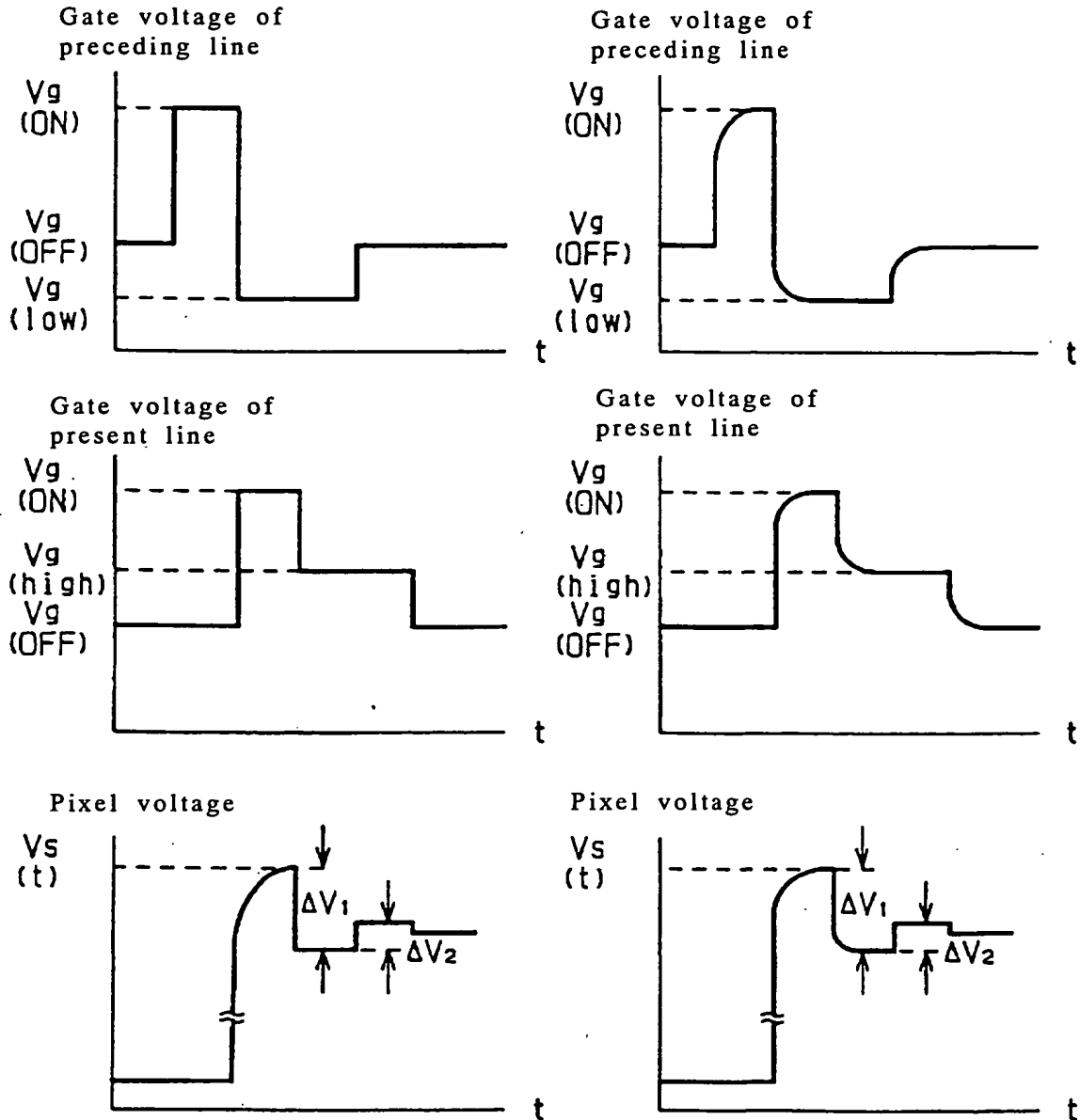


FIG. 15

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(Power feeding edge)

(Terminal edge)

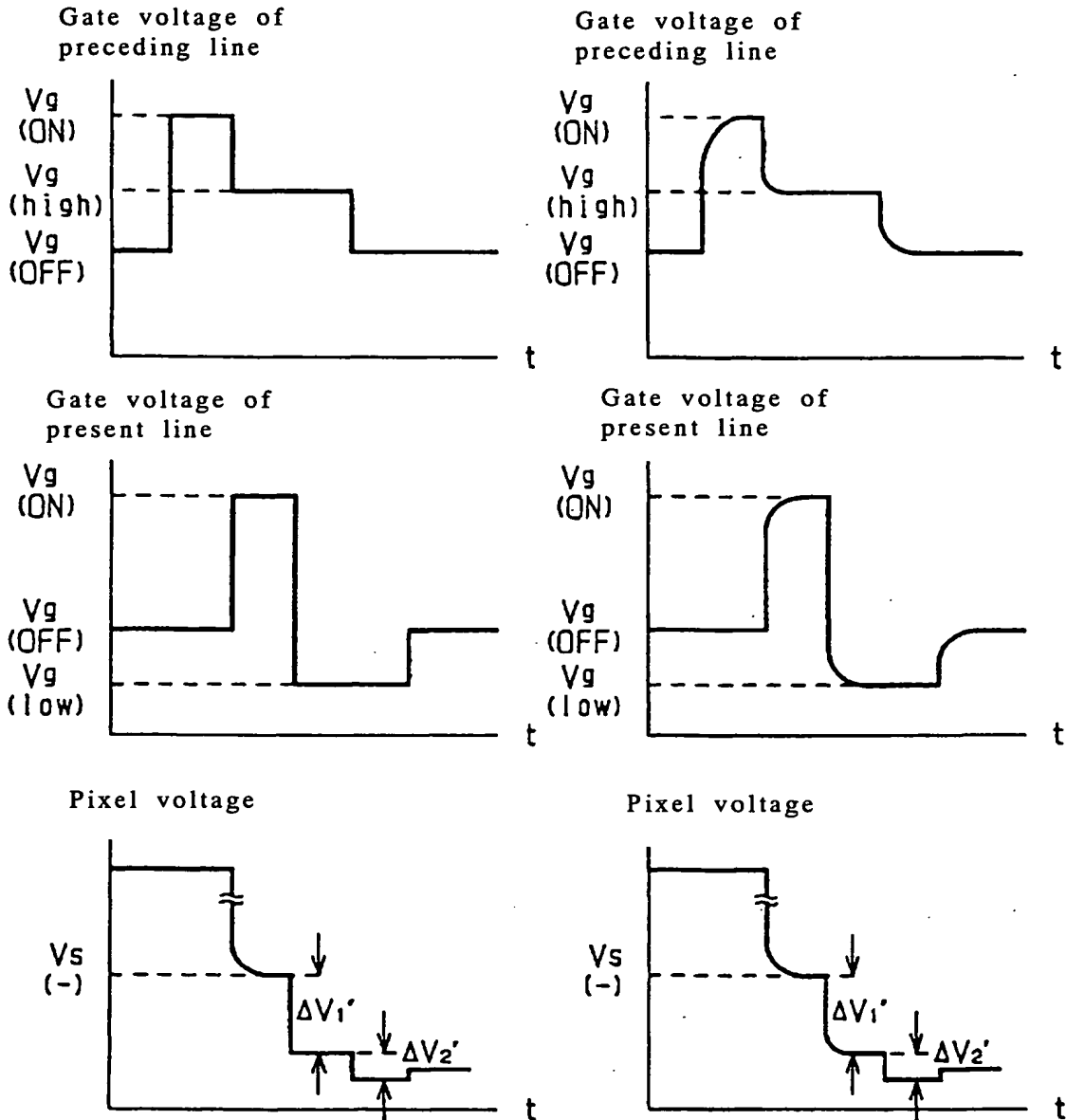


FIG. 16

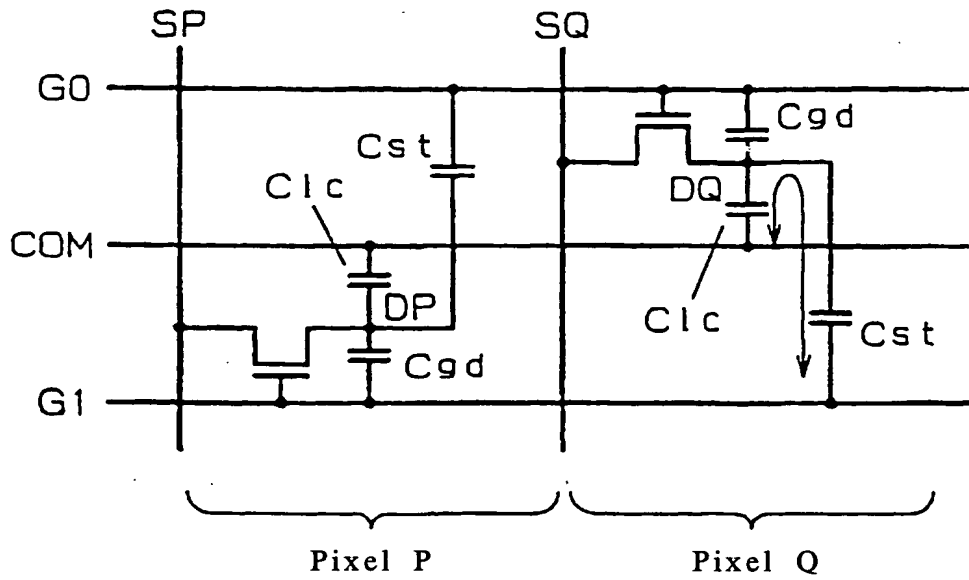


FIG. 17

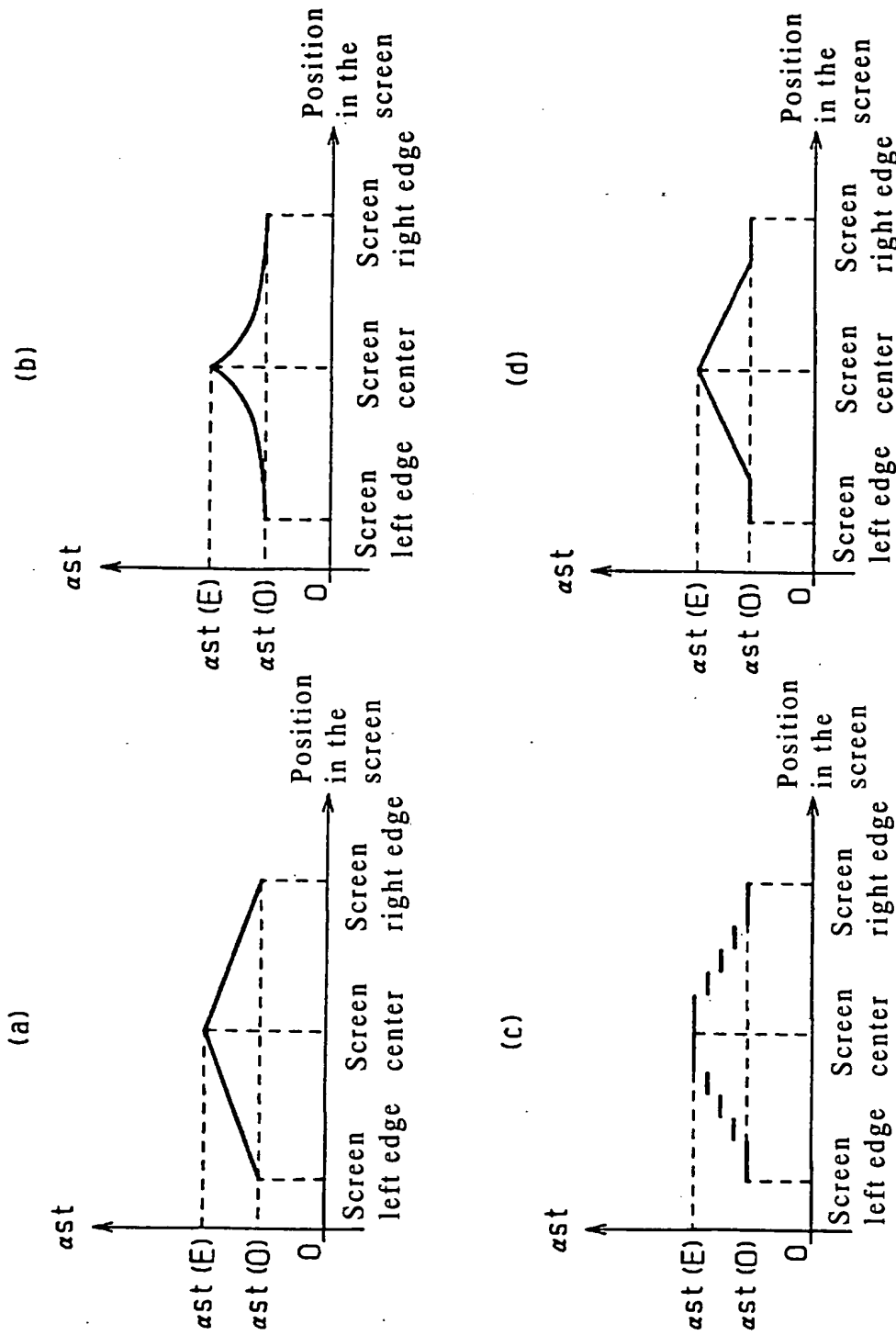


FIG. 18

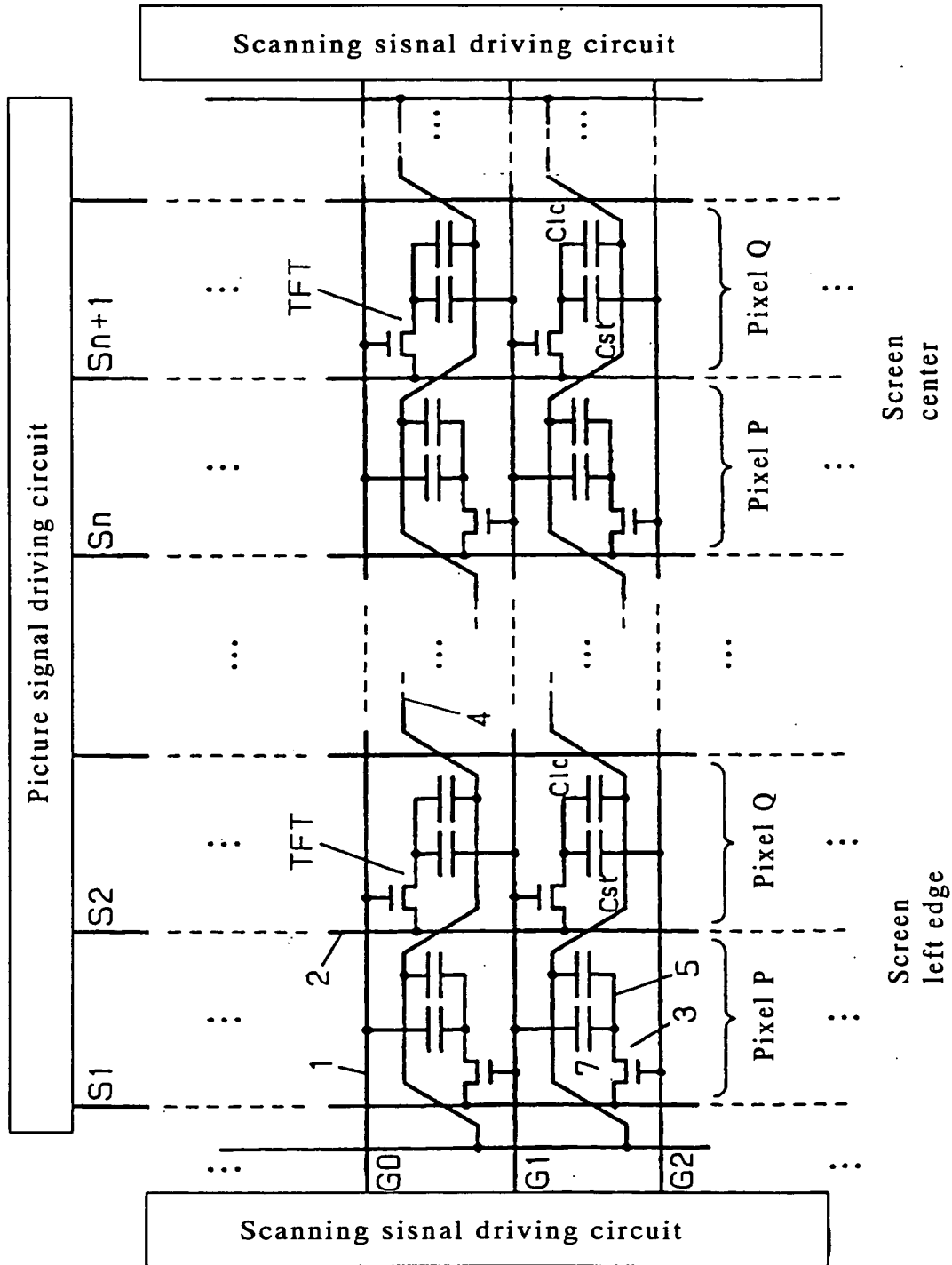


FIG. 19

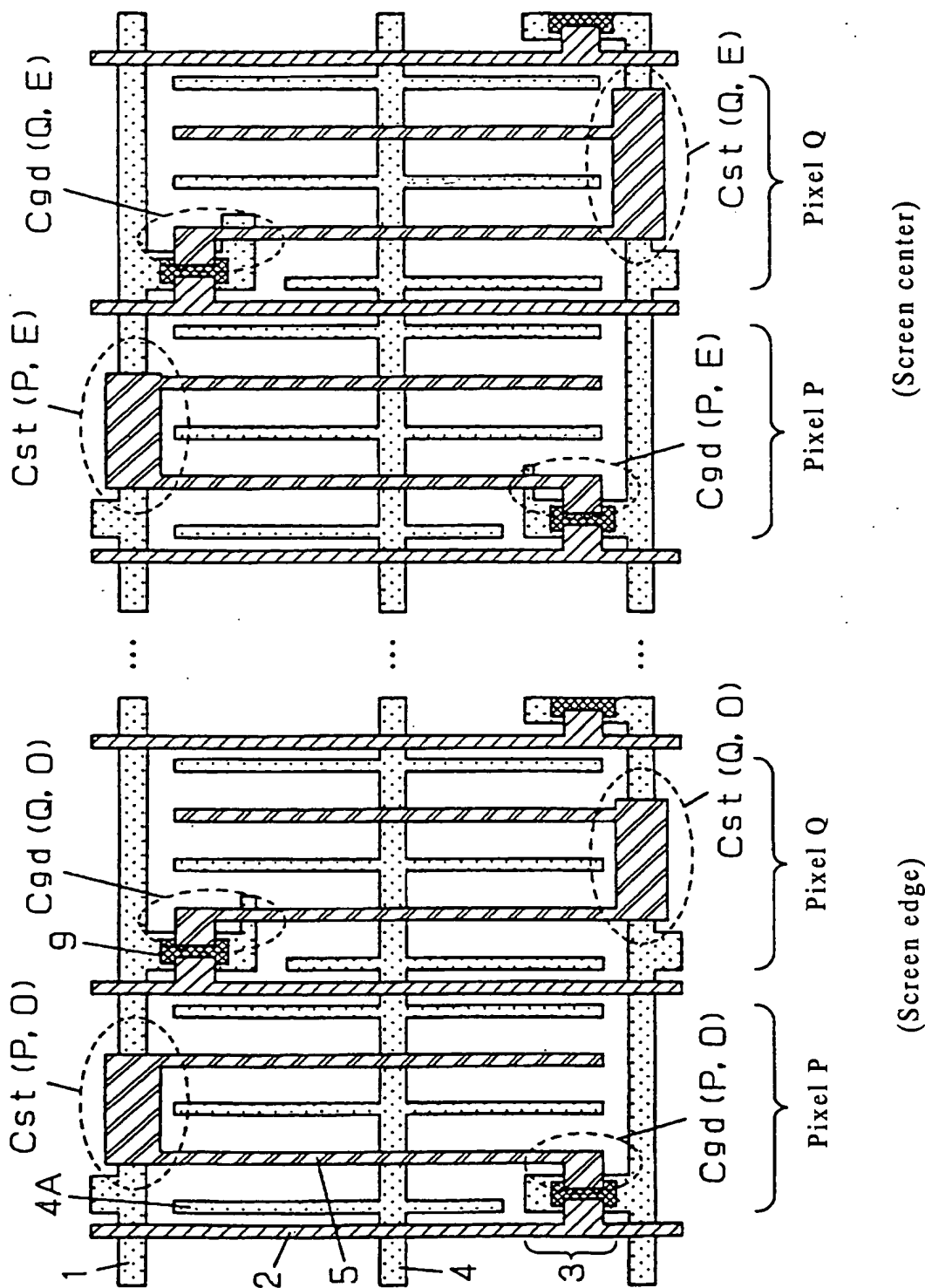


FIG. 20

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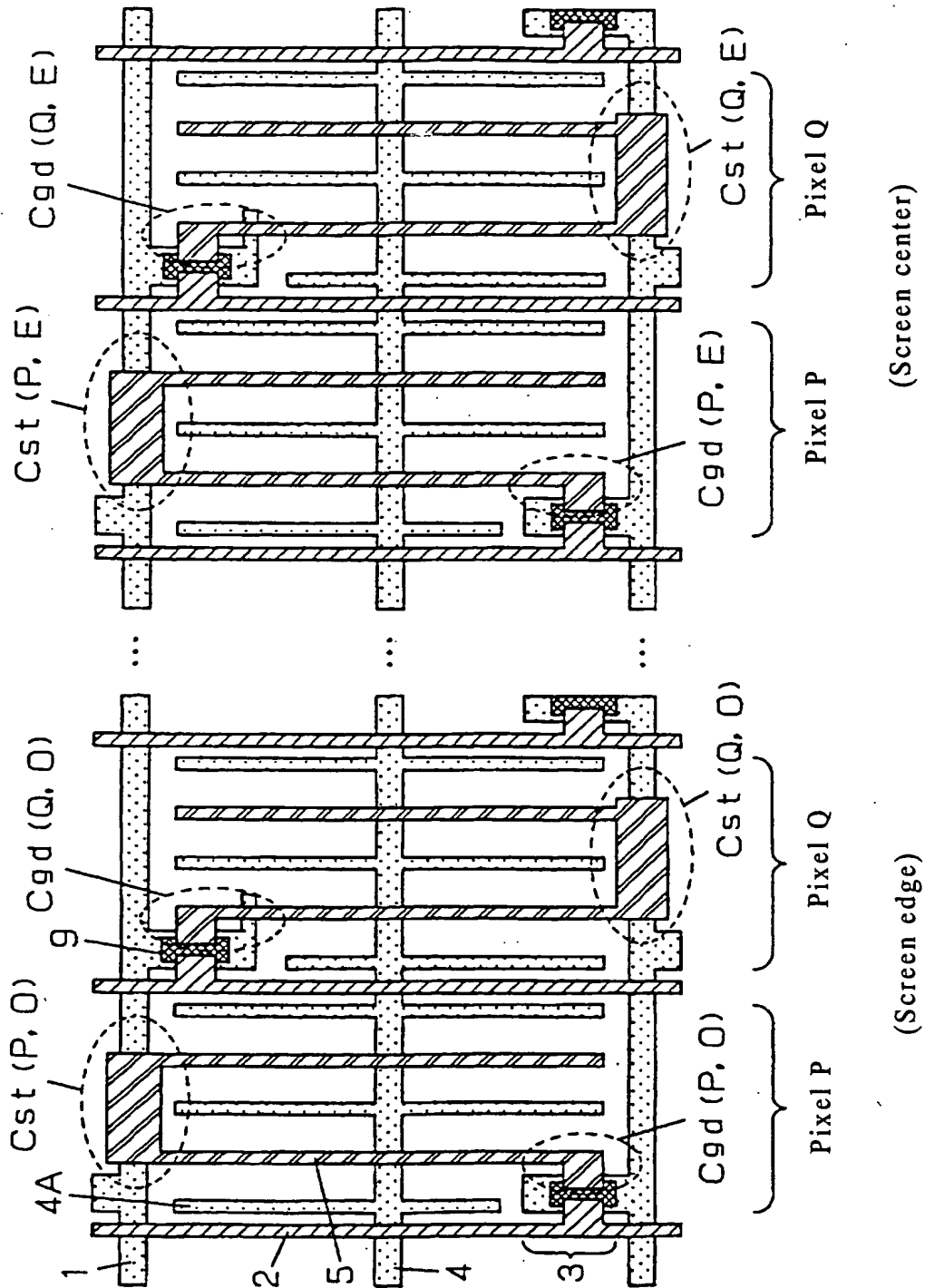


FIG. 21

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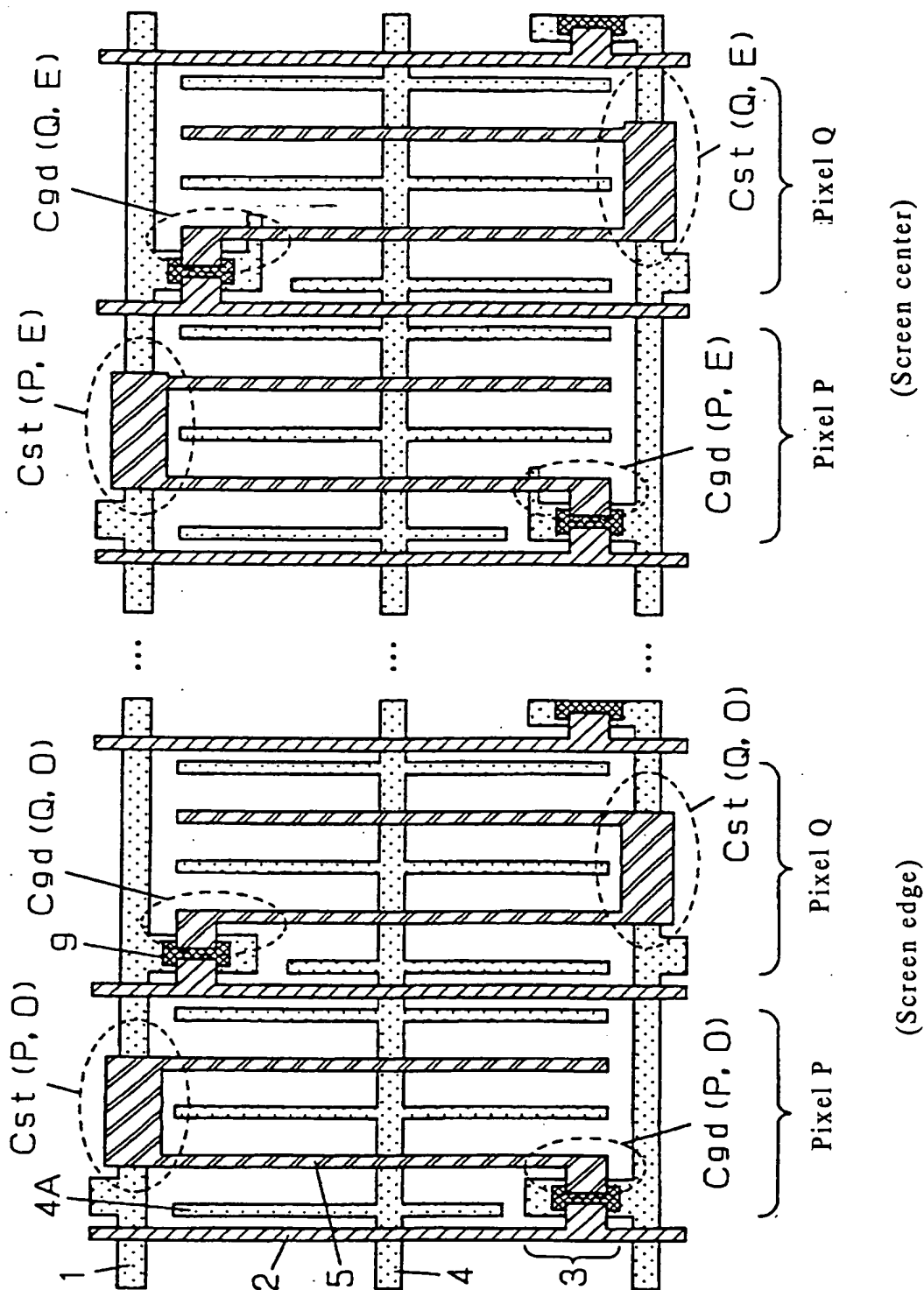


FIG. 22



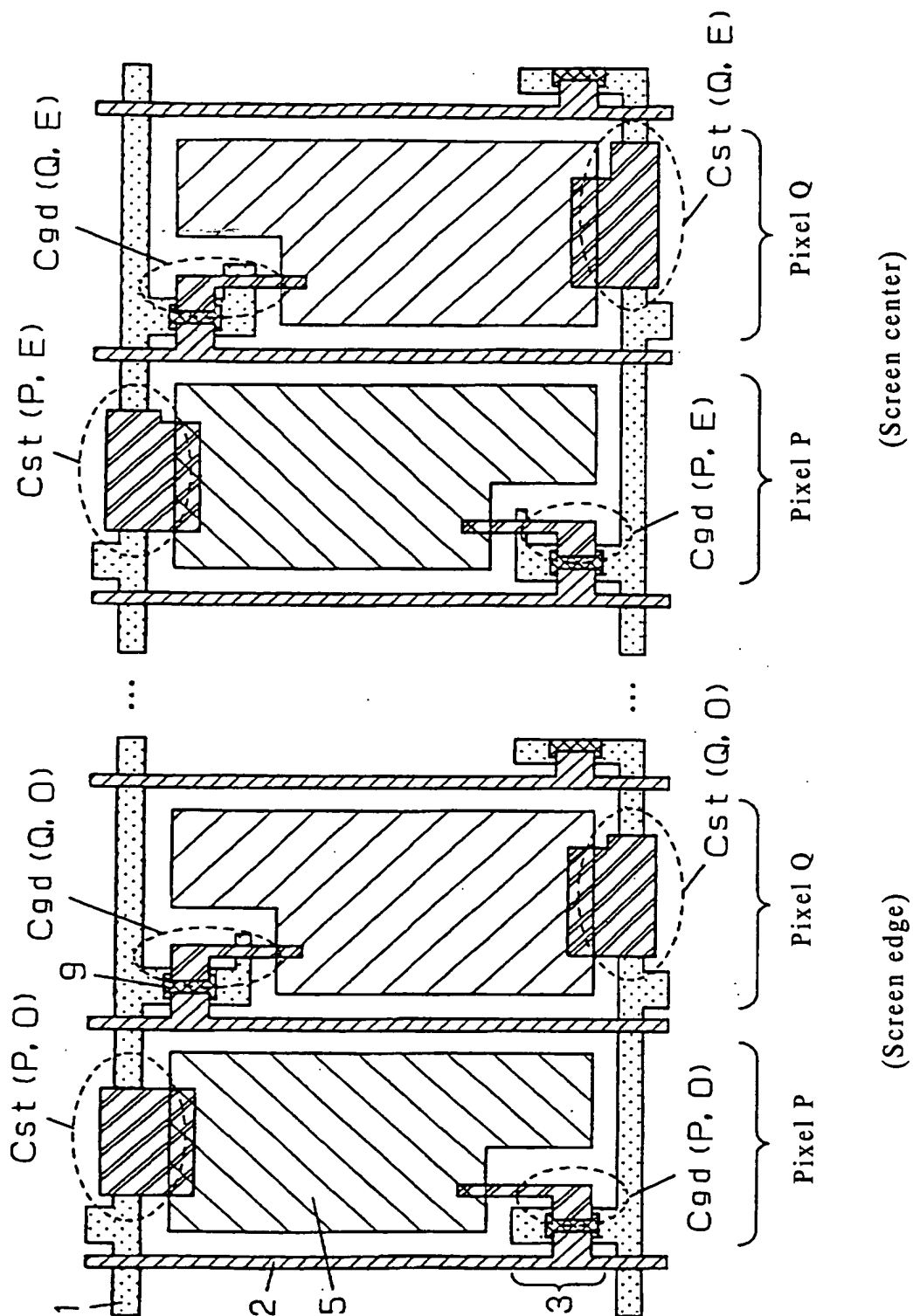


FIG. 24

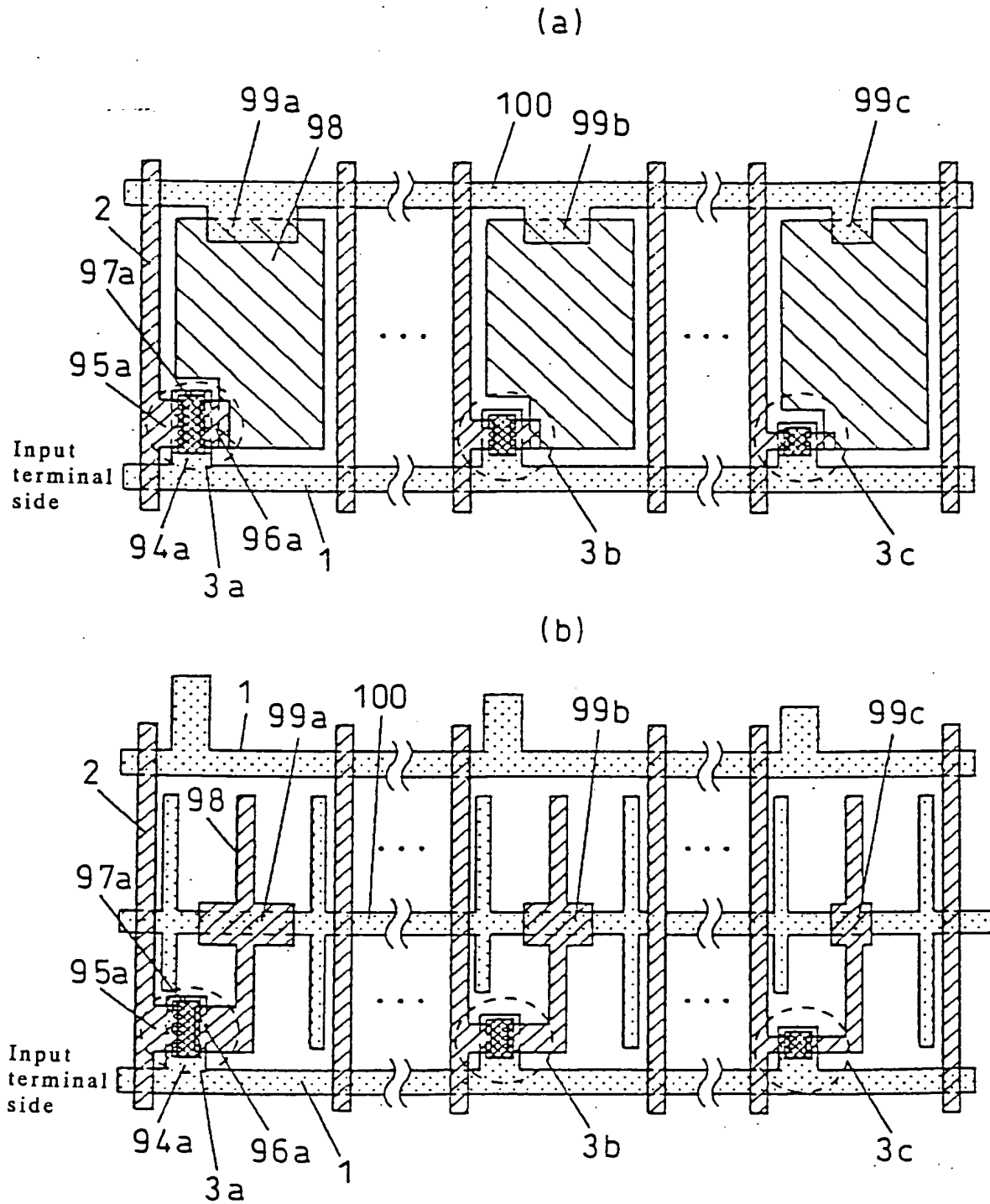


FIG. 25

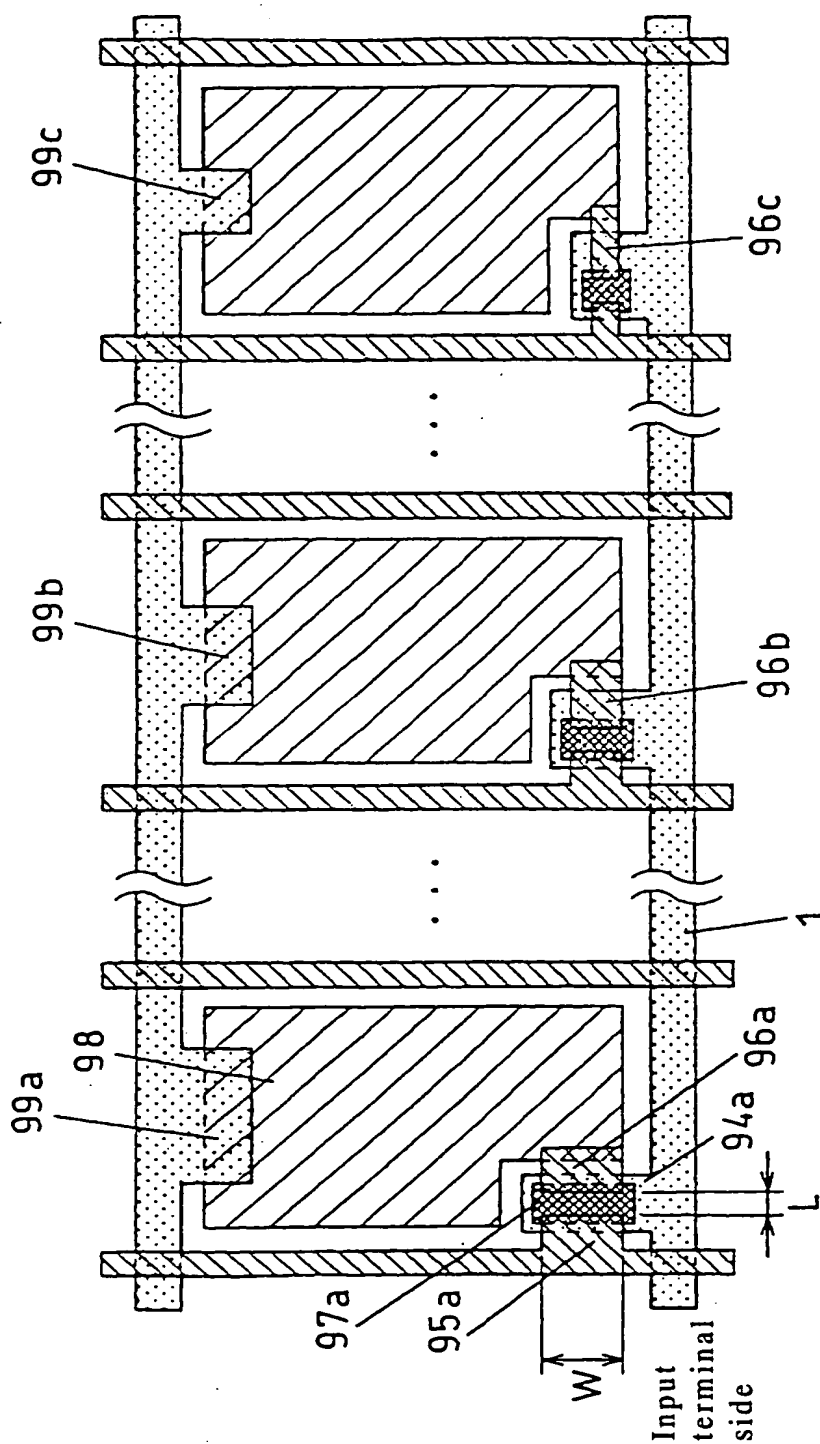
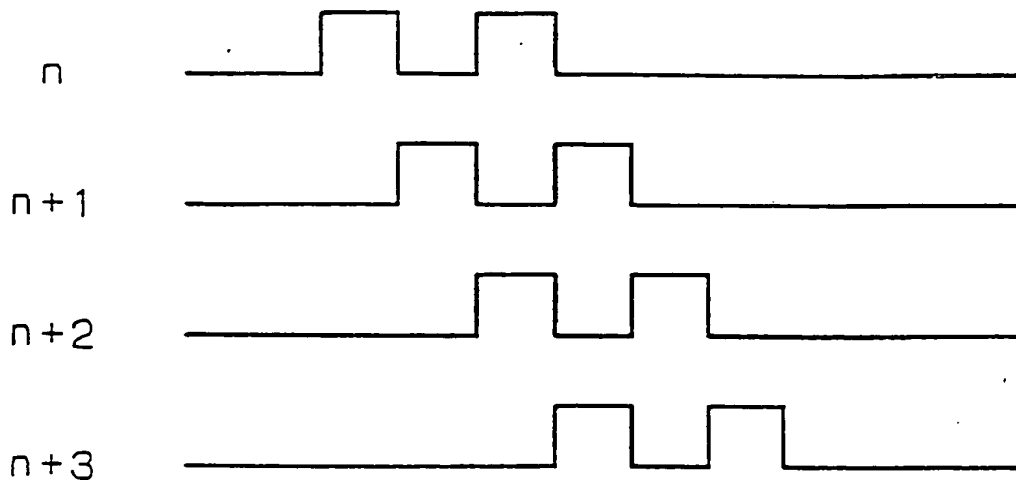


FIG. 26

(a)



(b)

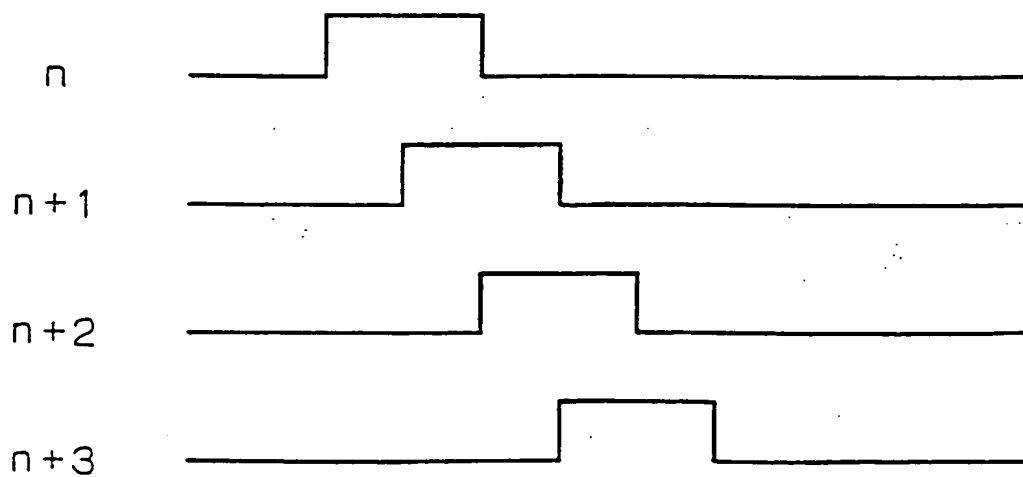


FIG. 27

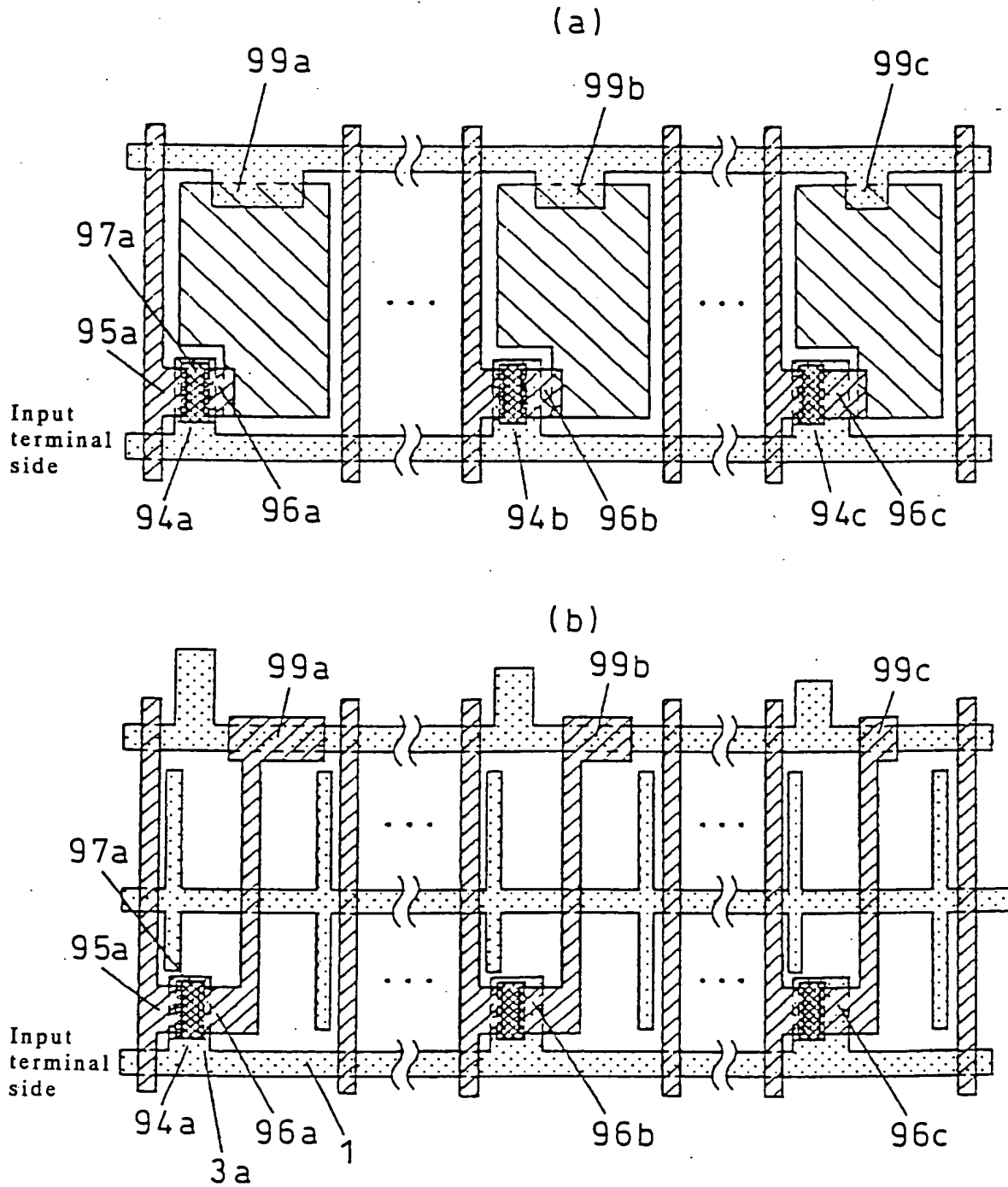
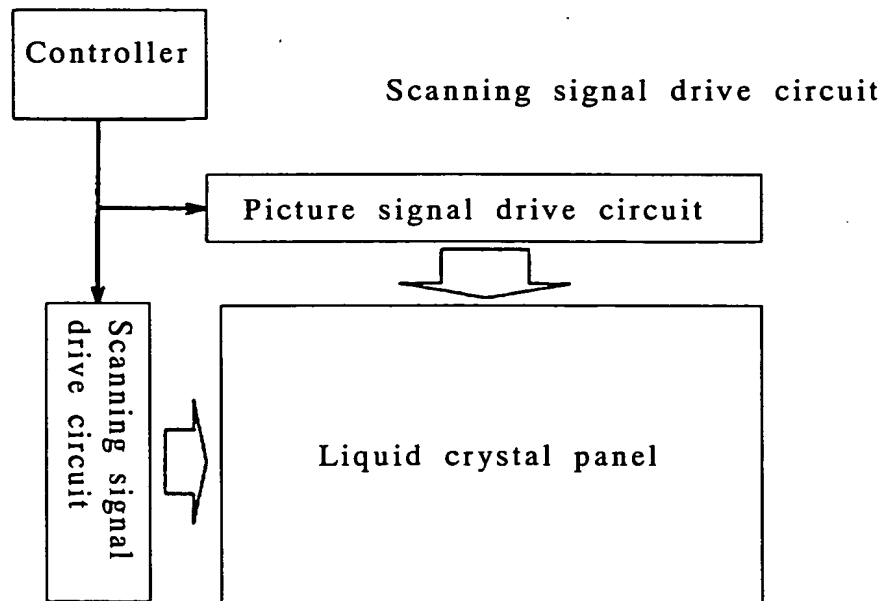


FIG. 28

(a)



(b)

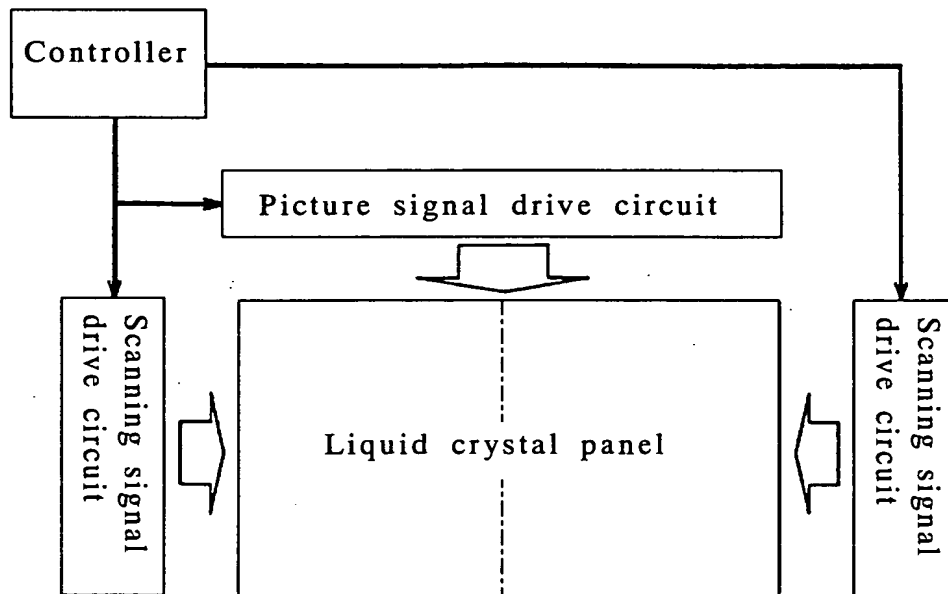


FIG. 29

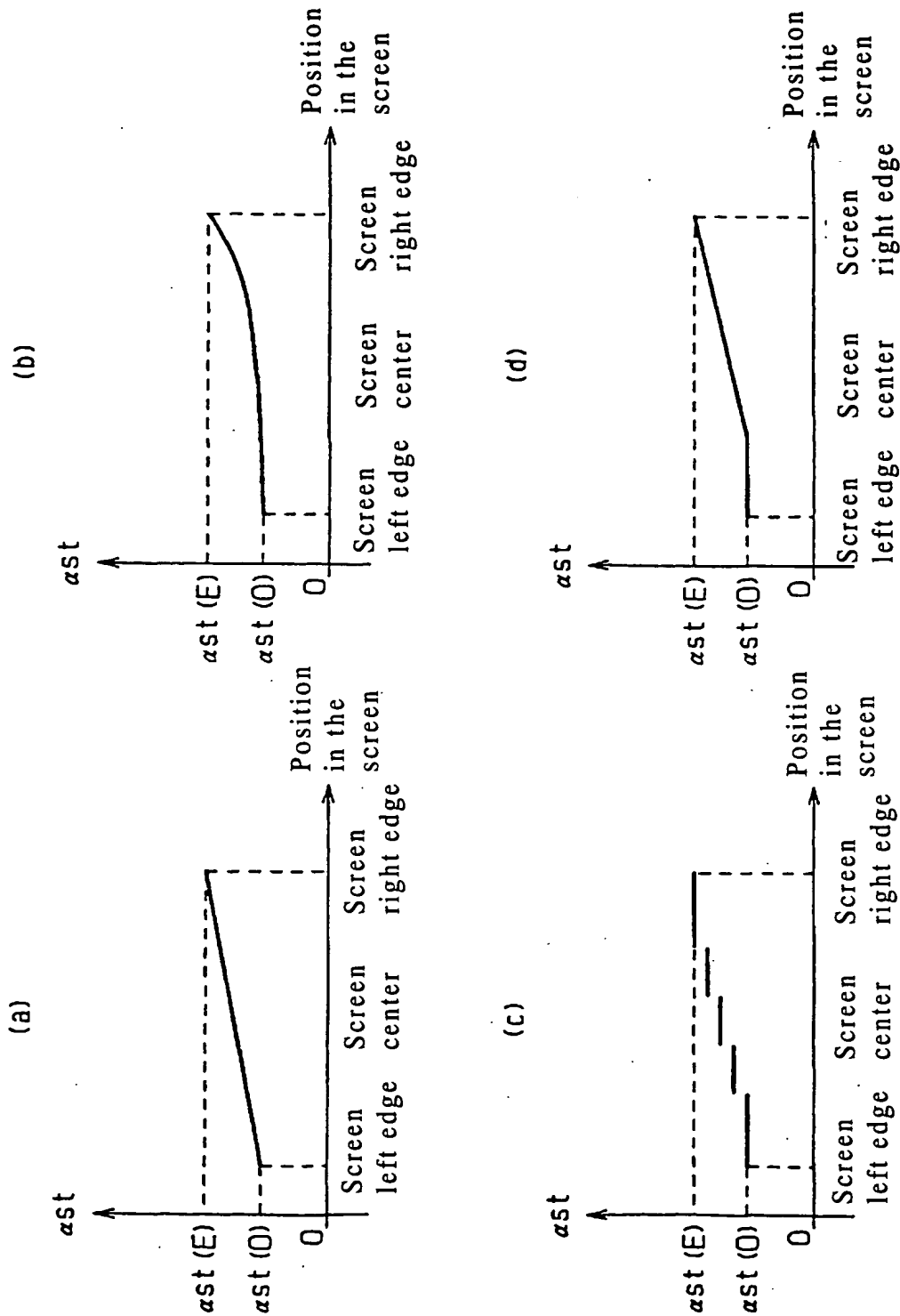
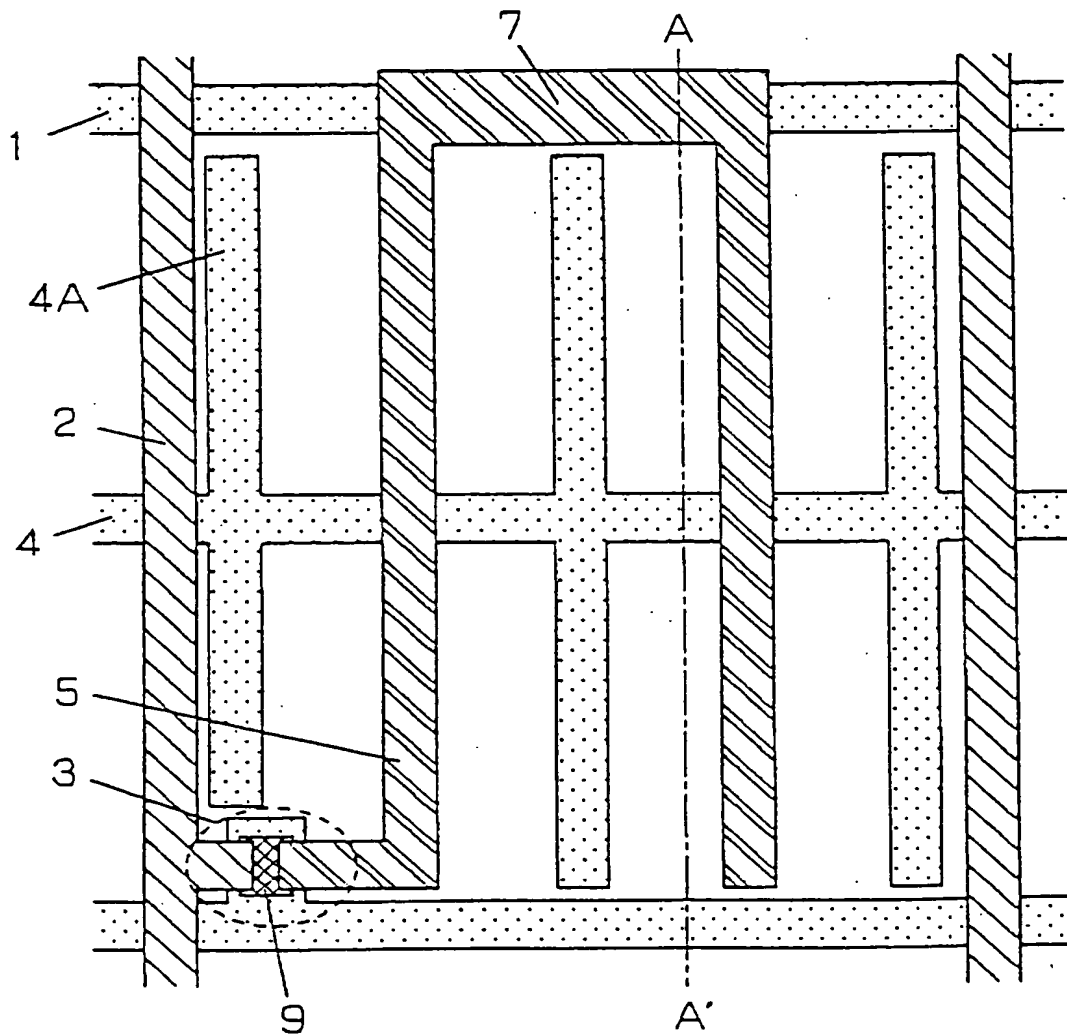


FIG. 30





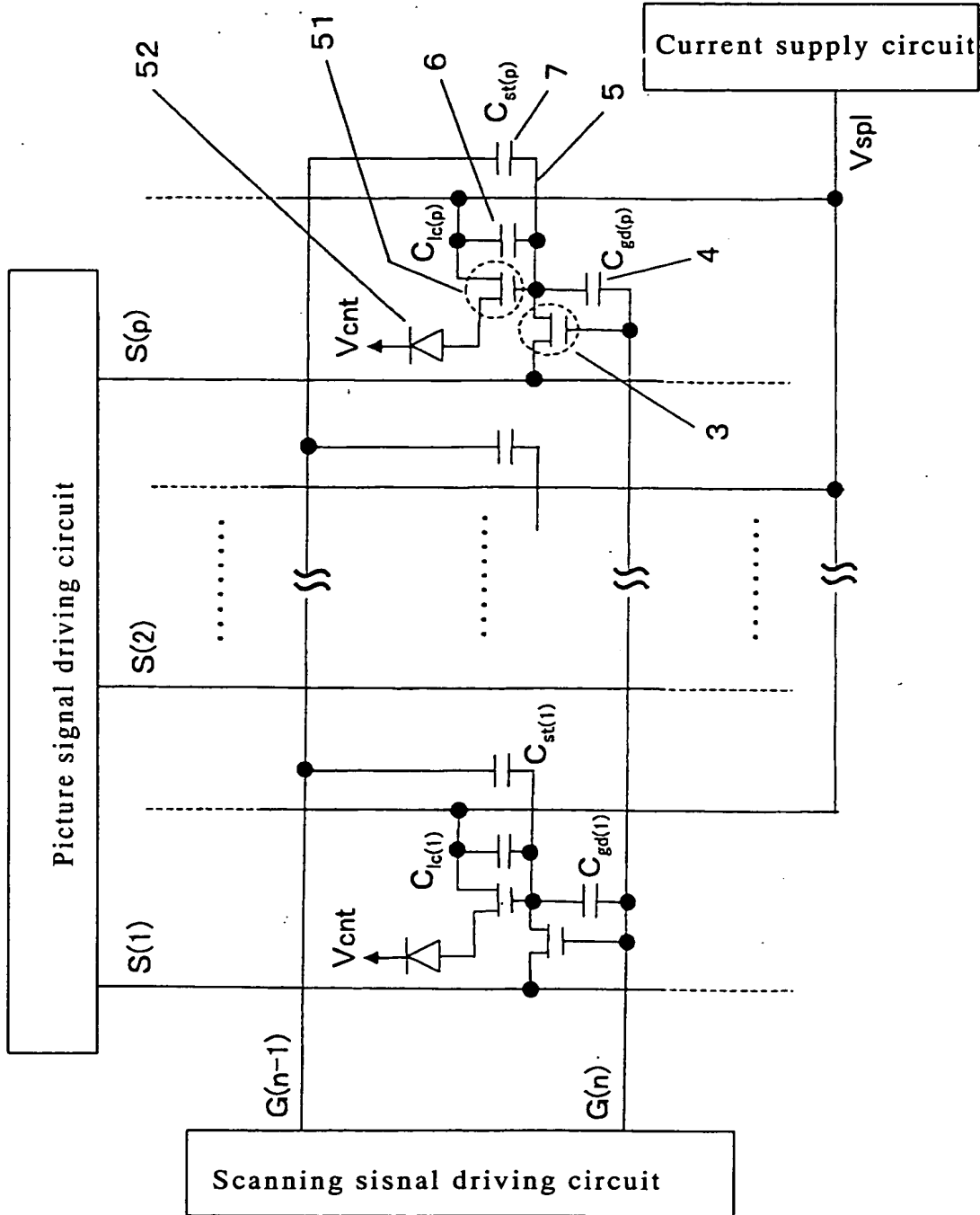


FIG. 33

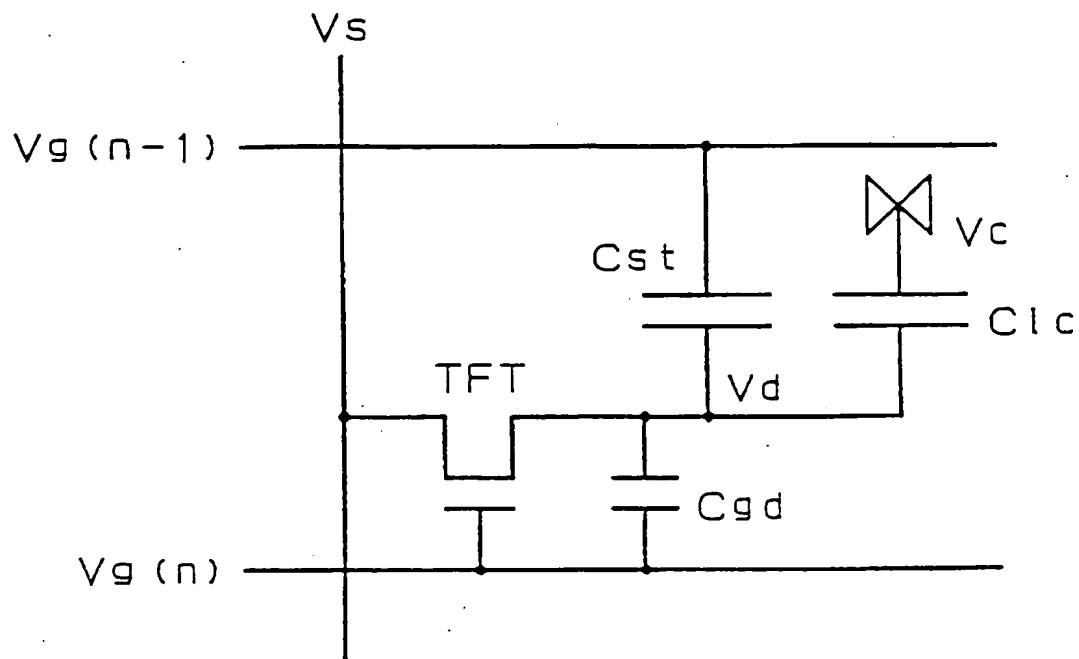


FIG. 34

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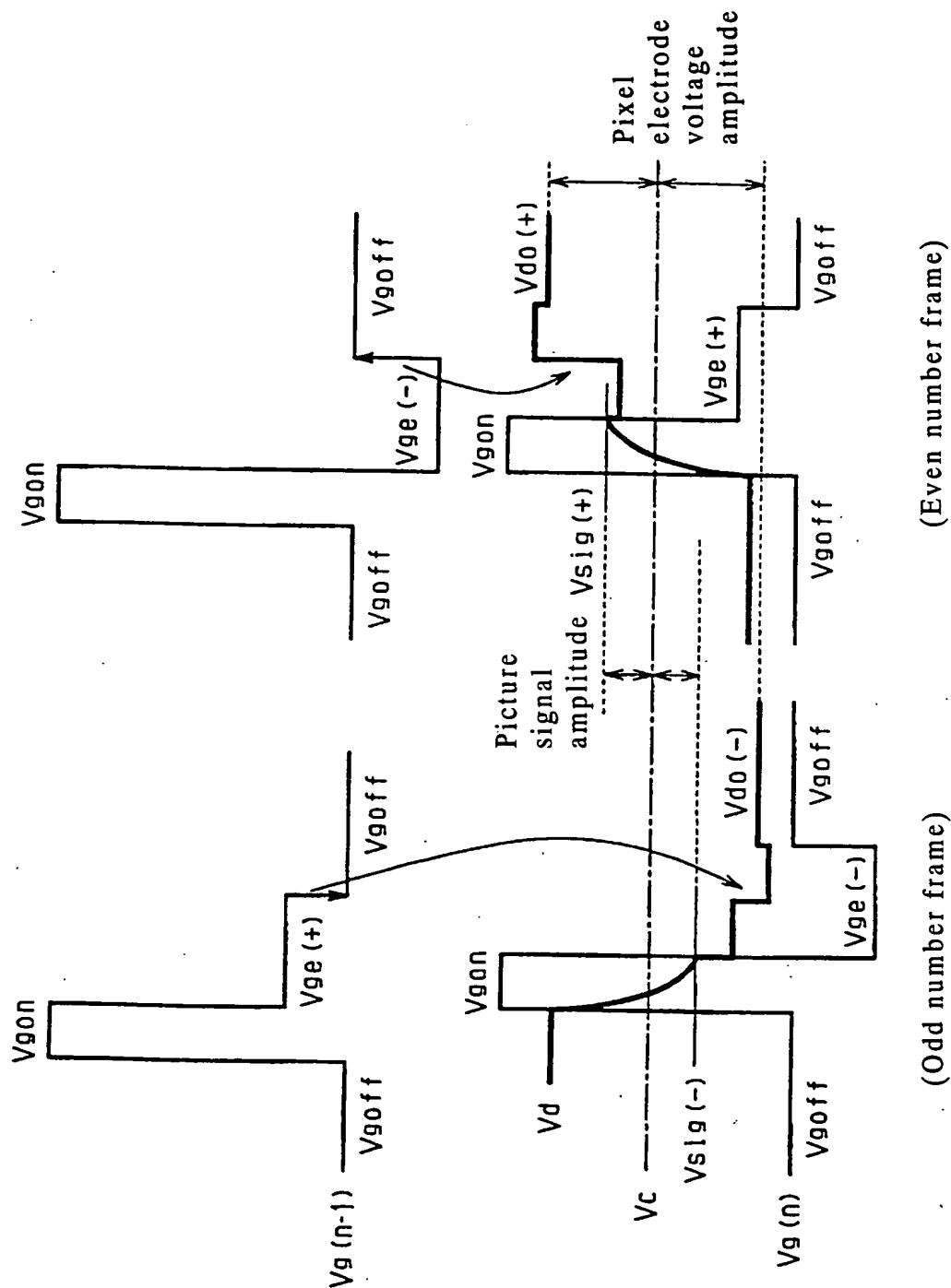


FIG. 35

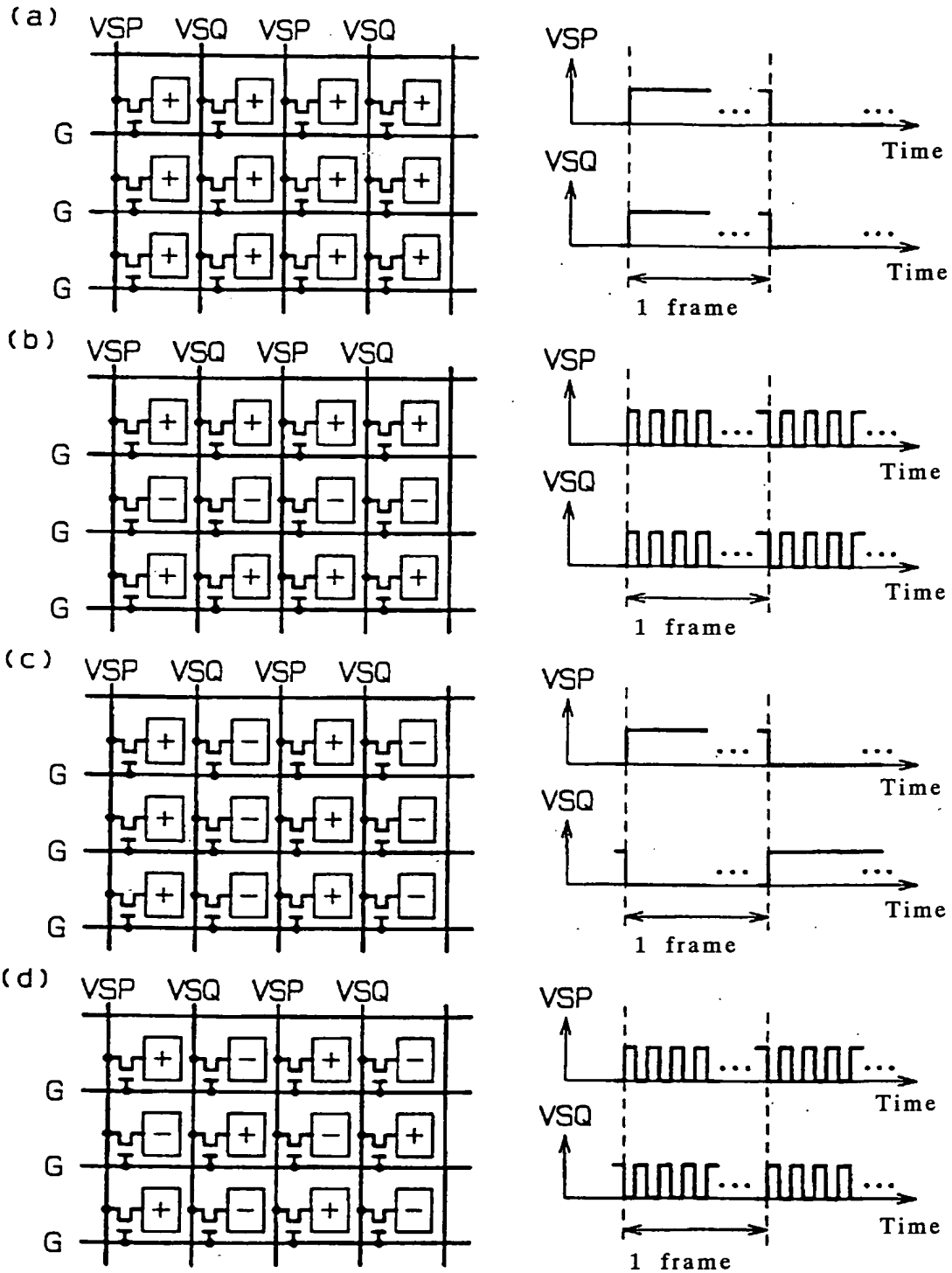


FIG. 36

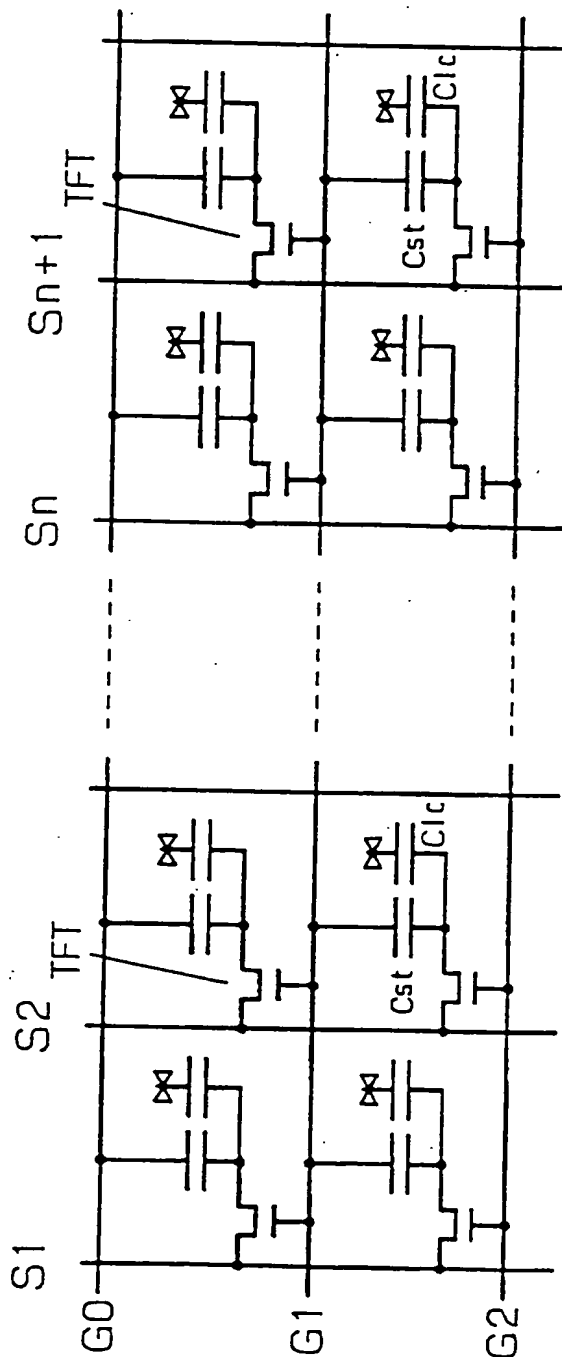


FIG. 37

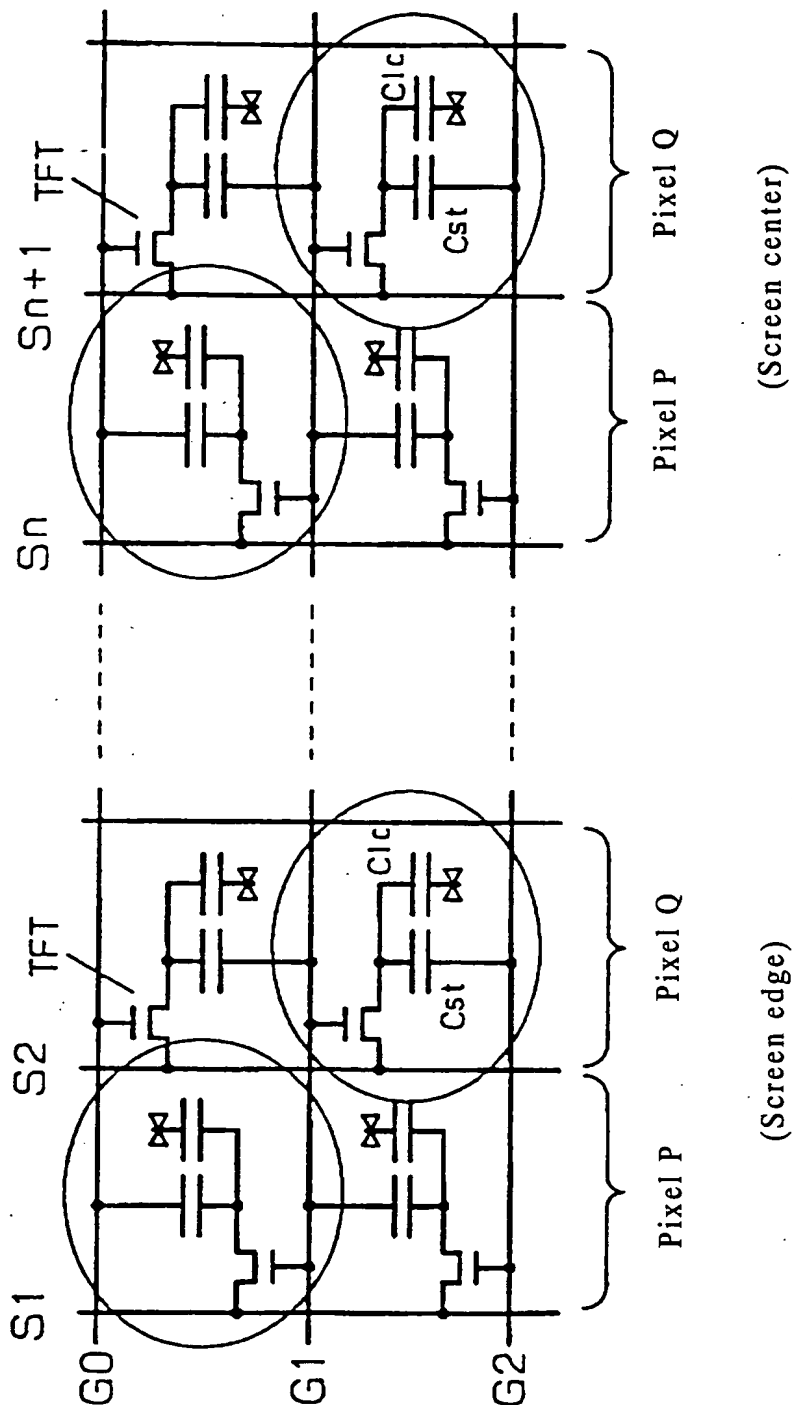


FIG. 38

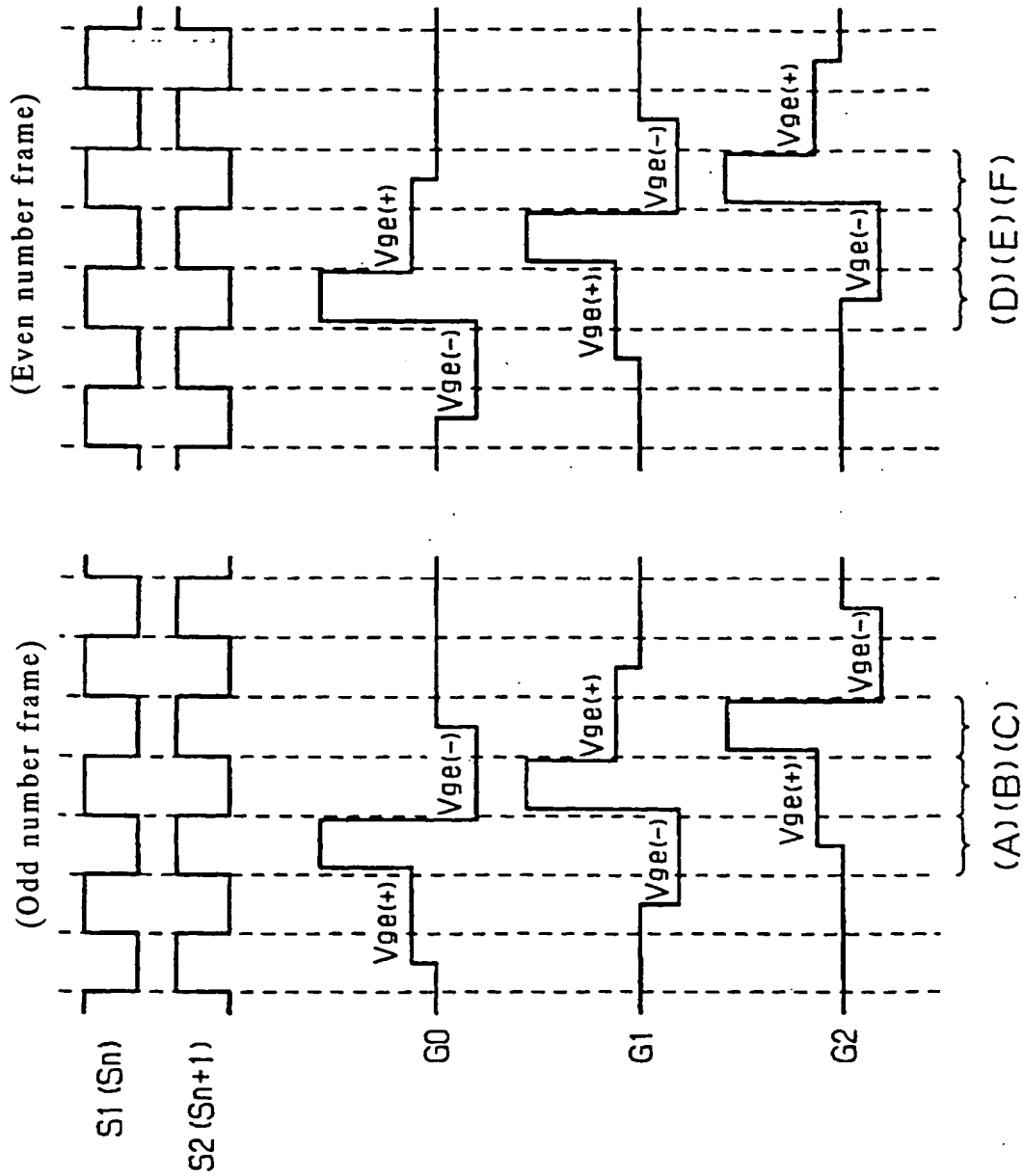


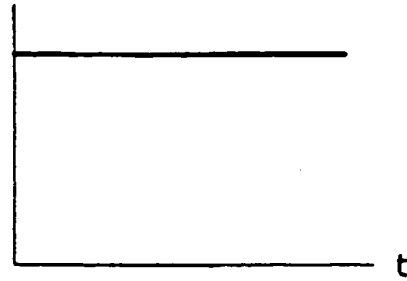
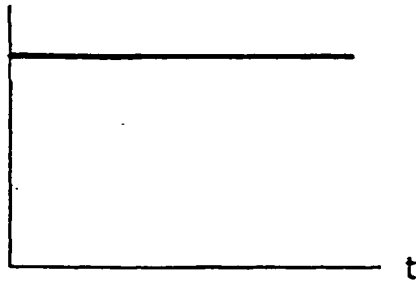
FIG. 39

(Power feeding edge)

(Terminal edge)

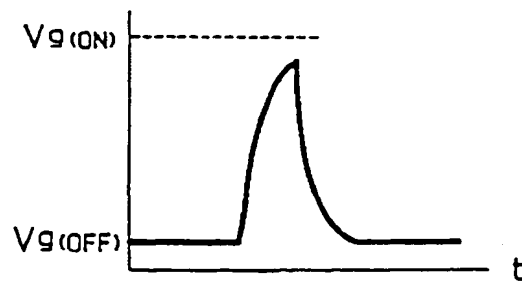
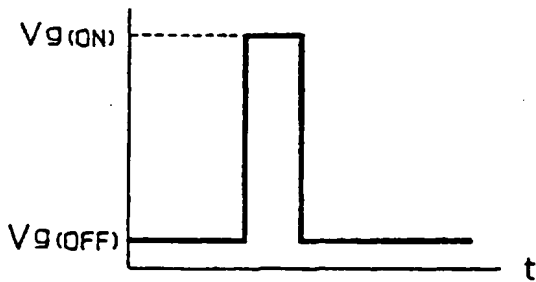
Source voltage

Source voltage



Gate voltage

Gate voltage



Pixel voltage

Pixel voltage

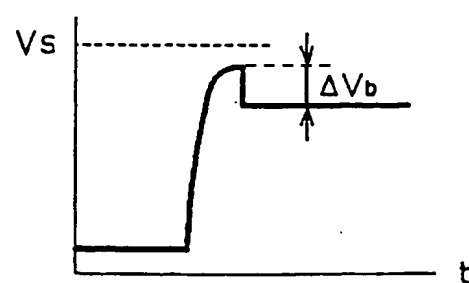
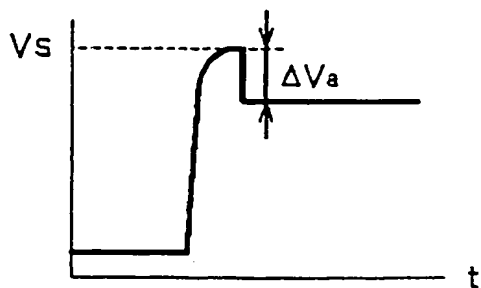
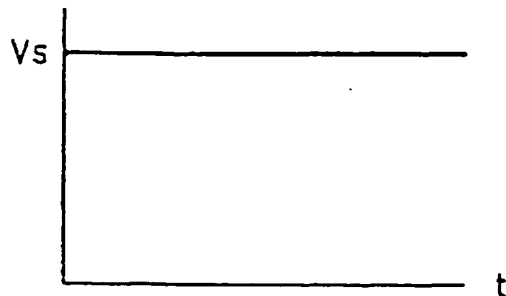


FIG. 40

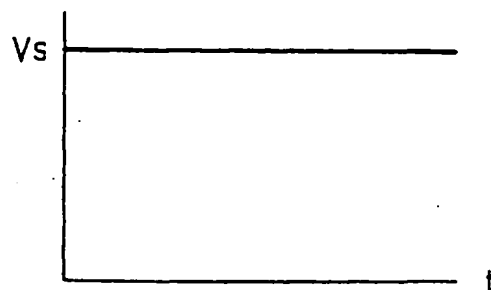
(Power feeding edge)

(Terminal edge)

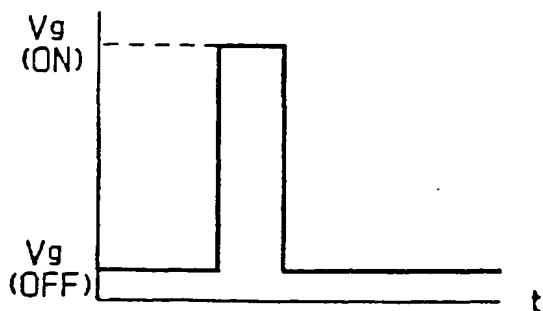
Source voltage



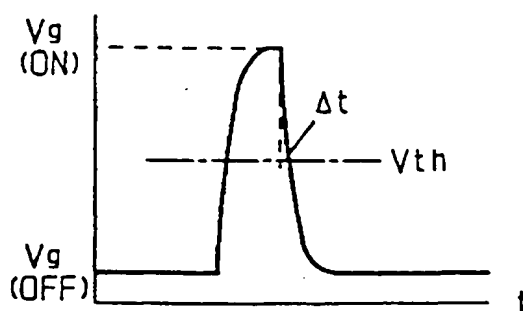
Source voltage



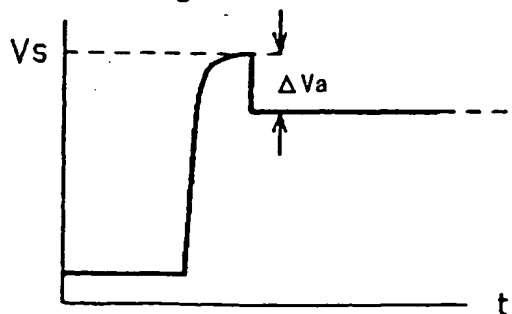
Gate voltage



Gate voltage



Pixel voltage



Pixel voltage

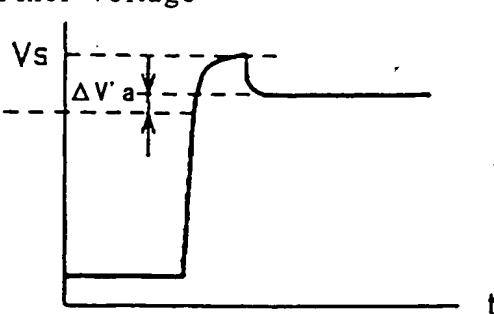


FIG. 41

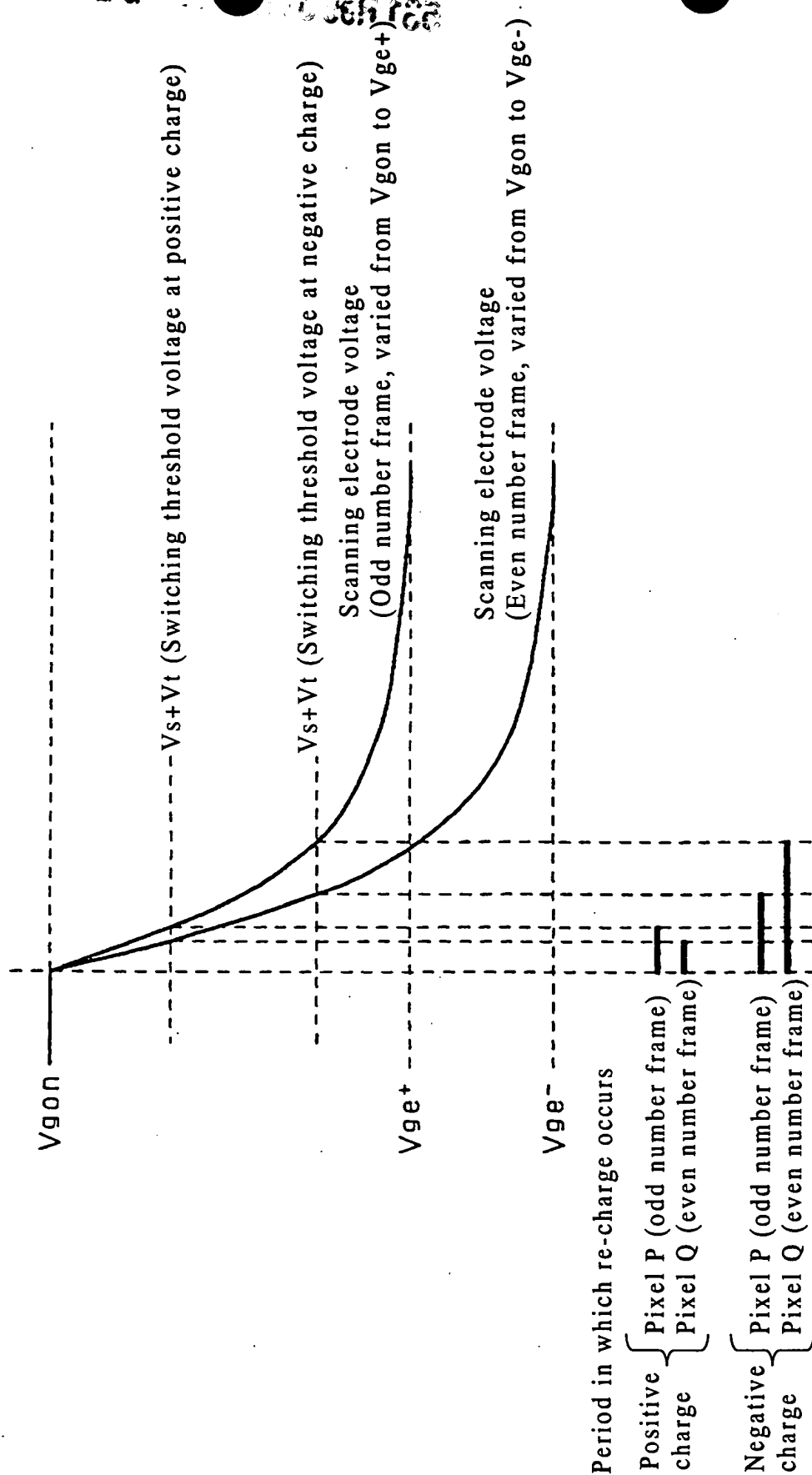


FIG. 42

(Power feeding edge)

(Terminal edge)

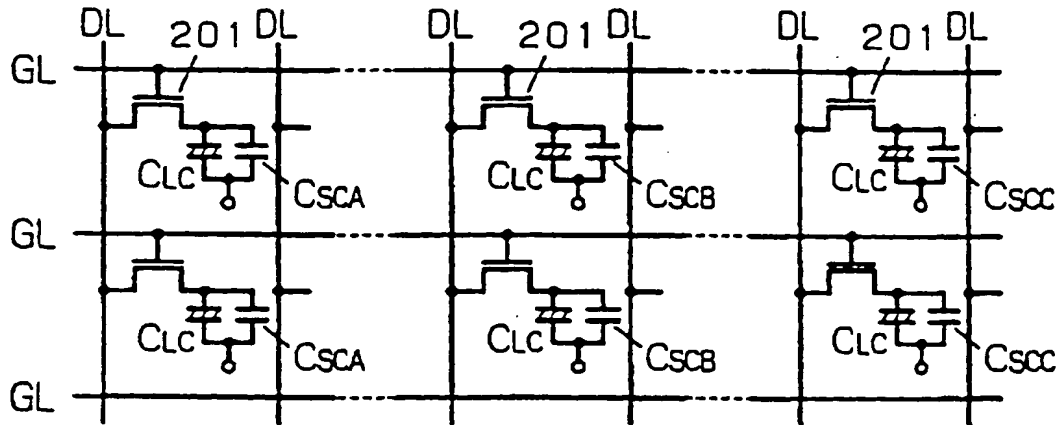


FIG. 43

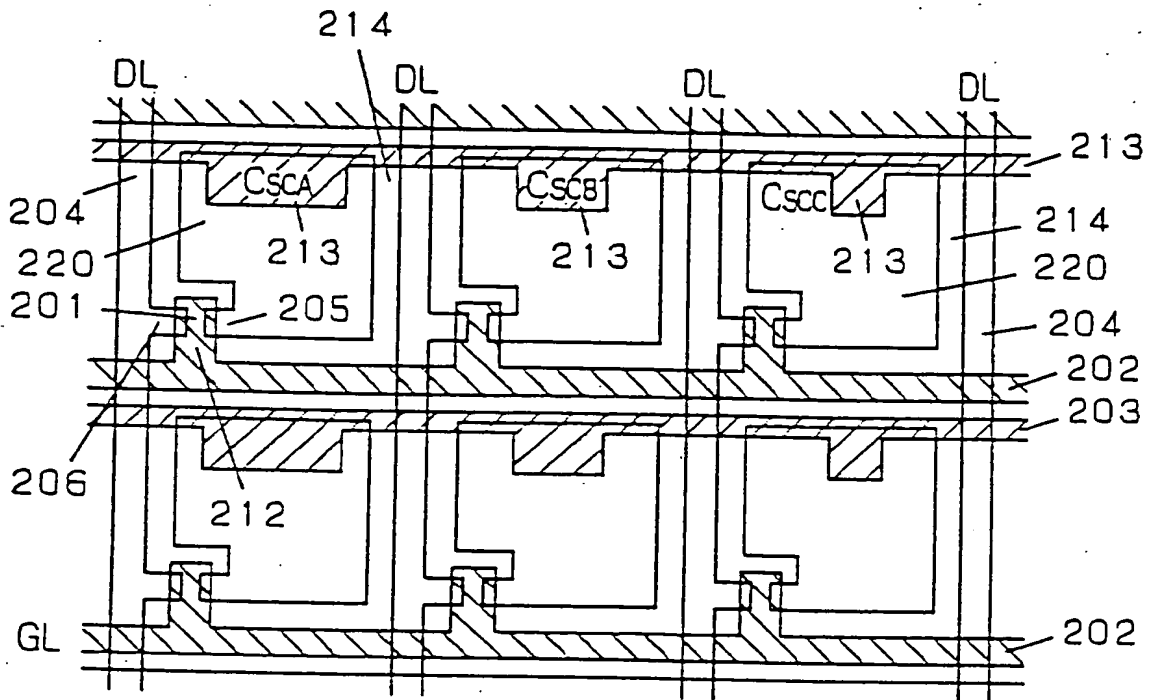


FIG. 44